

HSF Property: ROHS or Halogen-Free(5L3?)

LOUIS
XX BUILD : VRAM x4 pcs
www.aitech1.ru
Main Board
2011.XX.XX

XX BUILD: VRAM x 4pcs

SAMSUNG-Main Board: 1310A2468601 (2D)

HYNIX-Main Board: 1310A2468602 (2D)

SAMSUNG-Main Board: 1310A2468603 (3D)

HYNIX-Main Board: 1310A2468604 (3D)

MB PCB Board's IEC PN: 6050AXXXXXXX

21-OCT-2002		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER								
DESIGN								
CHECK								
RESPONSIBLE								
SIZE				VER	SIZE	CODE	DOC NUMBER	REV
FILE NAME					C	CS	1310xxxx-0-0	XD1
PN	XXX				SHEET			

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09. +V1.5 & +V0.75
10. +VPCIE & +VDDC_GPU
11. +VTT
12. +VCCSA
13. +VCC_CORE & +VGFX_CORE
14. +VCC_CORE & +VGFX_CORE
15. +V5S & +V3S
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Daughter Board

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72. LDPS BOARD
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74. POWER BOARD
75. 3D IR & ESD BOARD

SM BUS address / I2C

Cypress: 0xE0, 0x40

Audio amp: 0xC0

G-Sensor: 0x30, 0x32, 0x38, 0x3A

P-Sensor: 0x20, 0x21, 0x22, 0x23, 0xB0, 0xB1, 0xB2

DGPU Thermal: 0x82

Thermal IC: 0x98, 0x9A

Battery: 0x16

Charger IC: 0x12

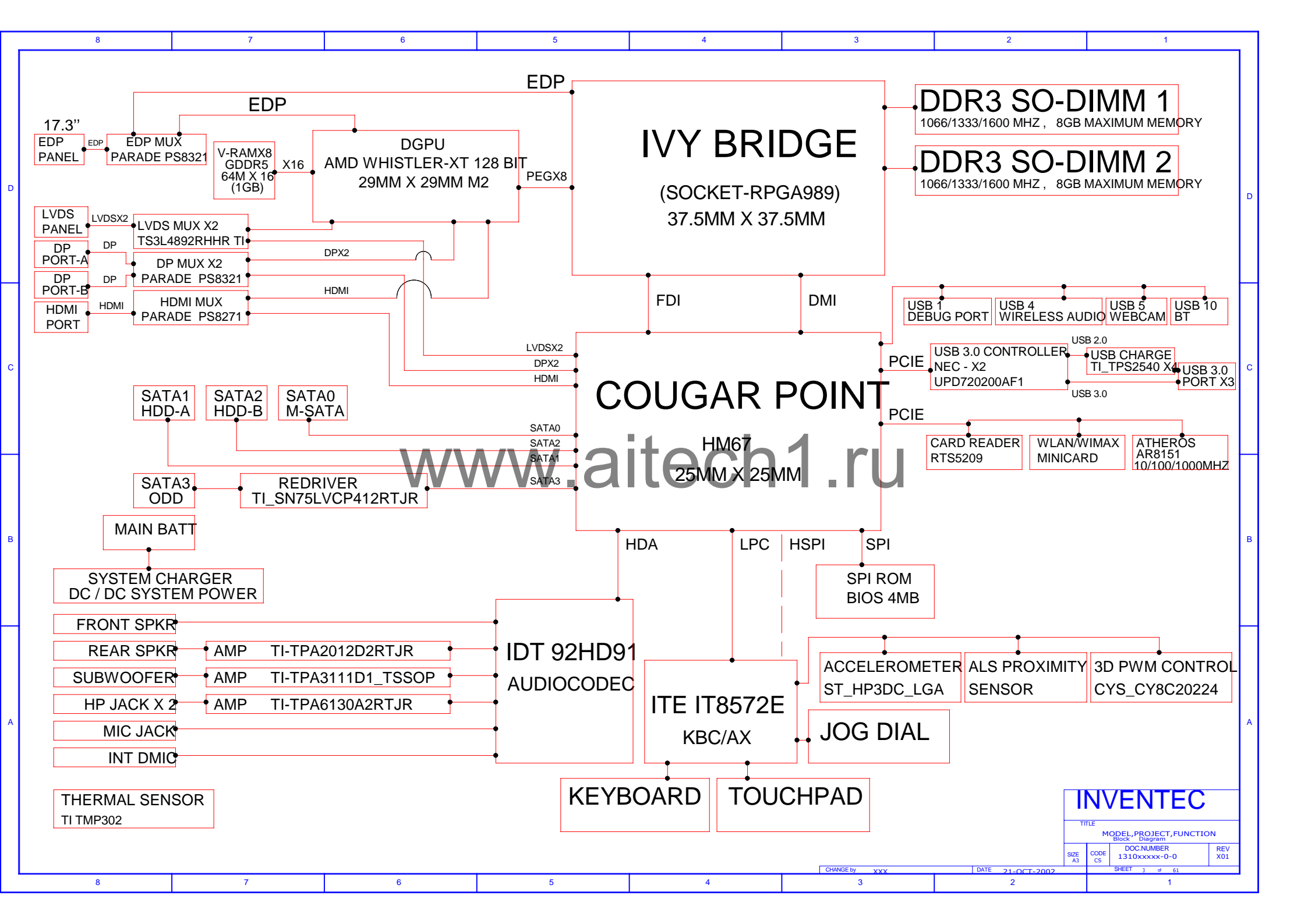
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
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IVY BRIDGE
(SOCKET-RPGA989)
37.5MM X 37.5MM

COUGAR POINT
www.aitech1.ru

HM67
25MM X 25MM

DDR3 SO-DIMM 1
1066/1333/1600 MHZ , 8GB MAXIMUM MEMORY

DDR3 SO-DIMM 2
1066/1333/1600 MHZ , 8GB MAXIMUM MEMORY

17.3"
EDP PANEL

EDP MUX
PARADE PS8321

V-RAMX8
GDDR5
64M X 16
(1GB)

X16

DGPU
AMD WHISTLER-XT 128 BIT
29MM X 29MM M2

PEGX8

LVDSX2
LVDS MUX X2
TS3L4892RHHR TI

DPX2

DP MUX X2
PARADE PS8321

HDMI MUX
PARADE PS8271

HDMI

SATA1
HDD-A

SATA2
HDD-B

SATA0
M-SATA

SATA3
ODD

REDRIVER
TI_SN75LVCP412RTJR

MAIN BATT

SYSTEM CHARGER
DC / DC SYSTEM POWER

FRONT SPKR

REAR SPKR

SUBWOOFER

HP JACK X 2

MIC JACK

INT DMIC

THERMAL SENSOR
TI TMP302

USB 1
DEBUG PORT

USB 4
WIRELESS AUDIO

USB 5
WEBCAM

USB 10
BT

USB 3.0 CONTROLLER
NEC - X2
UPD720200AF1

USB 2.0
CHARGE
TI_TPS2540 X4

USB 3.0
PORT X3

CARD READER
RTS5209

WLAN/WIMAX
MINICARD

ATHEROS
AR8151
10/100/1000MHZ

SATA0
SATA2
SATA1
SATA3

IDT 92HD91
AUDIOCODEC

ITE IT8572E
KBC/AX

SPI ROM
BIOS 4MB

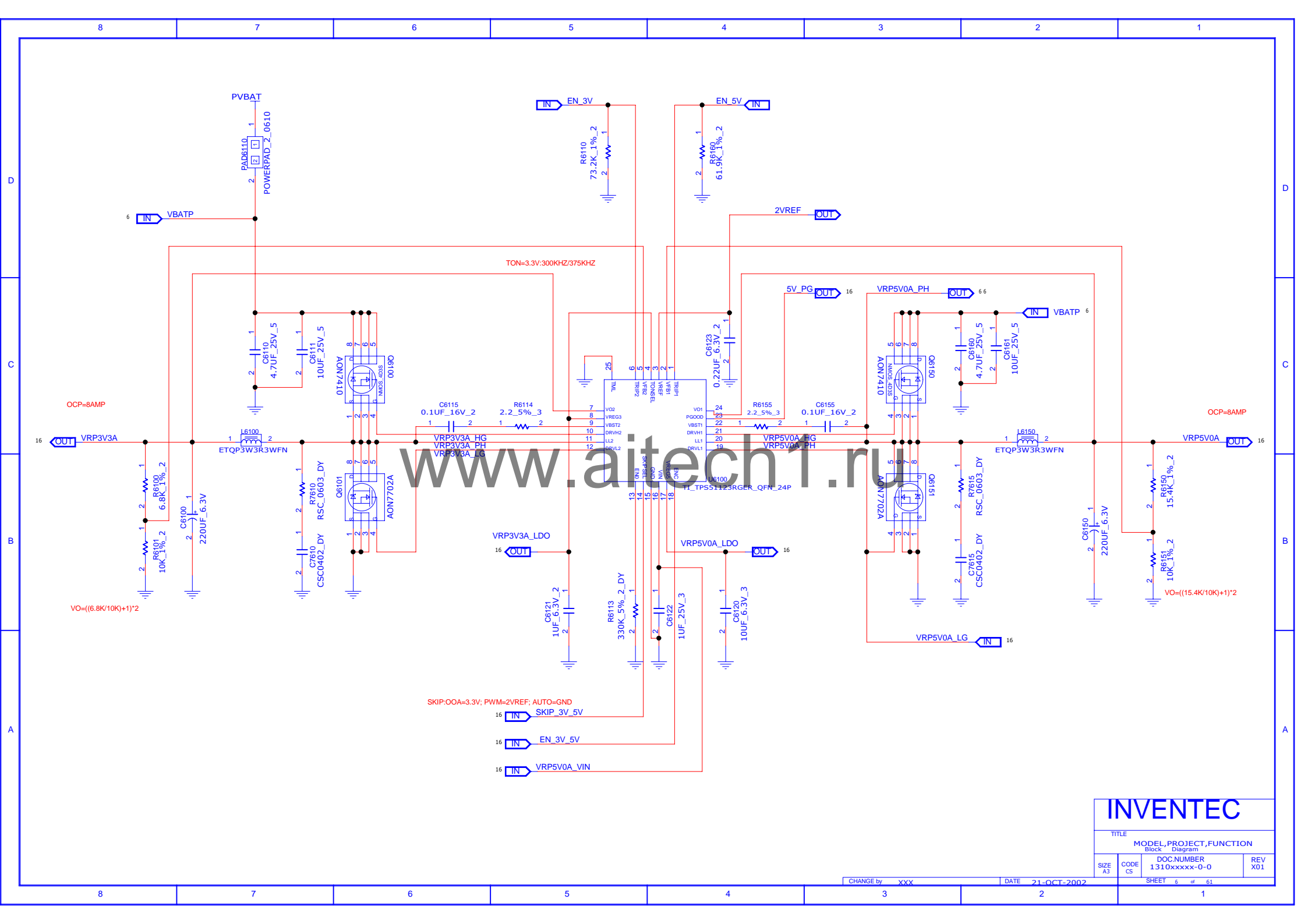
ACCELEROMETER
ST_HP3DC_LGA

ALS PROXIMITY
SENSOR

3D PWM CONTROL
CYS_CY8C20224

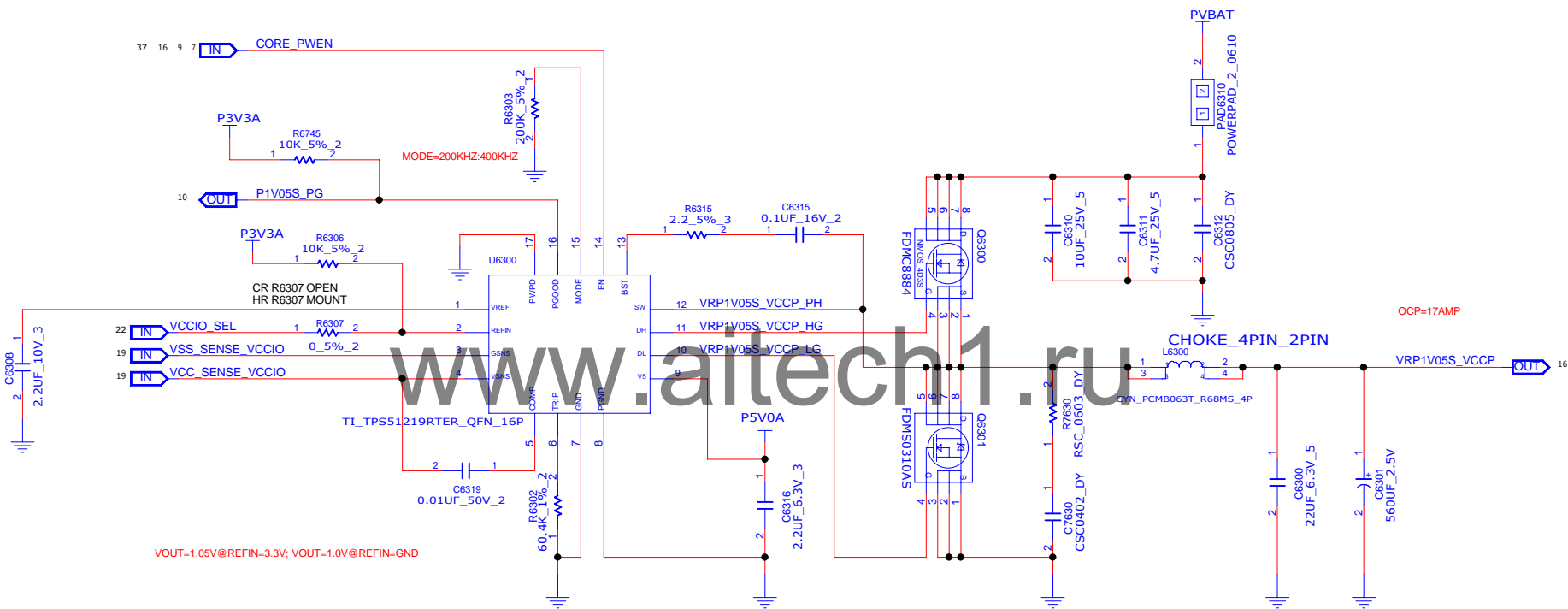
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INVENTEC			
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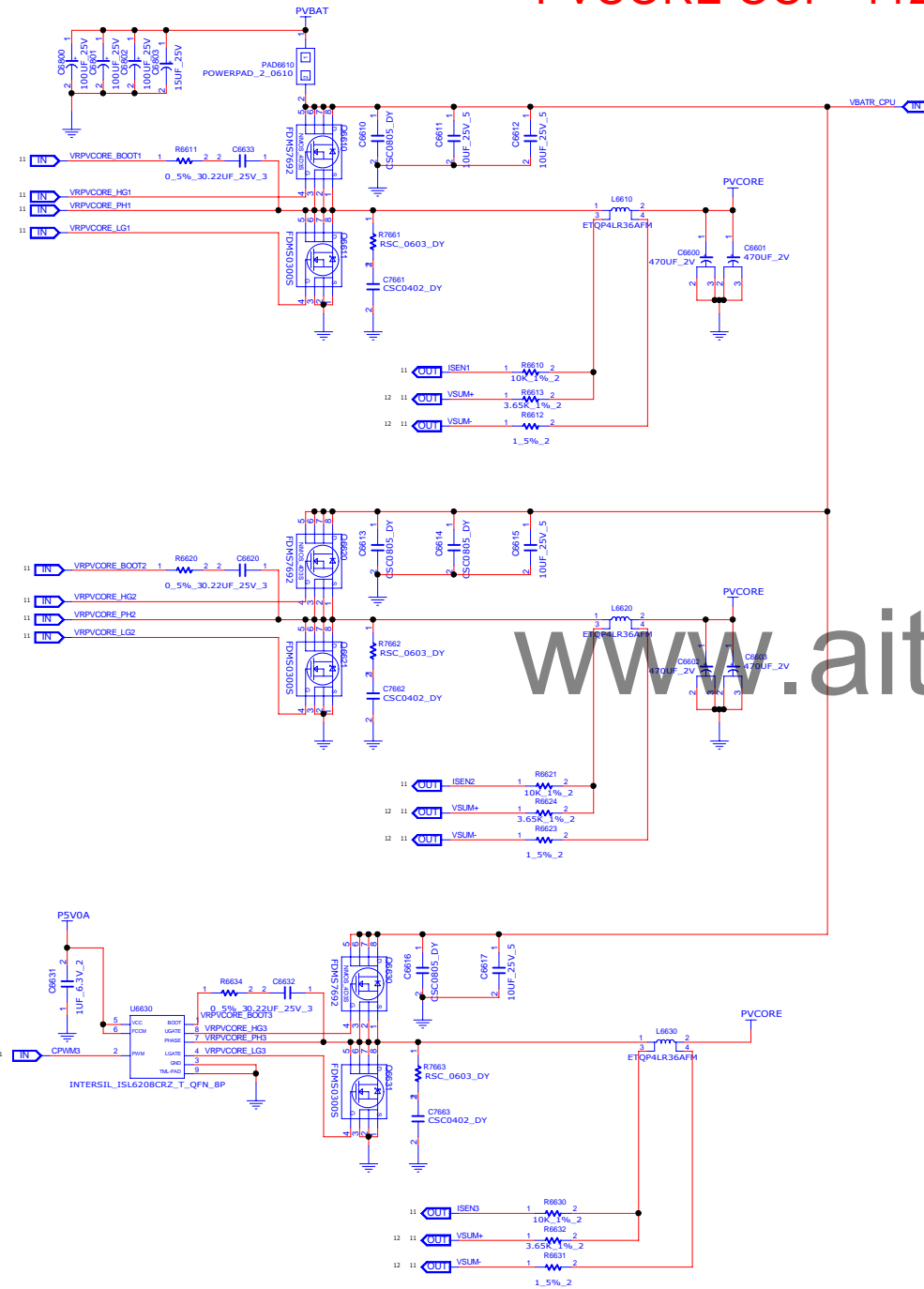
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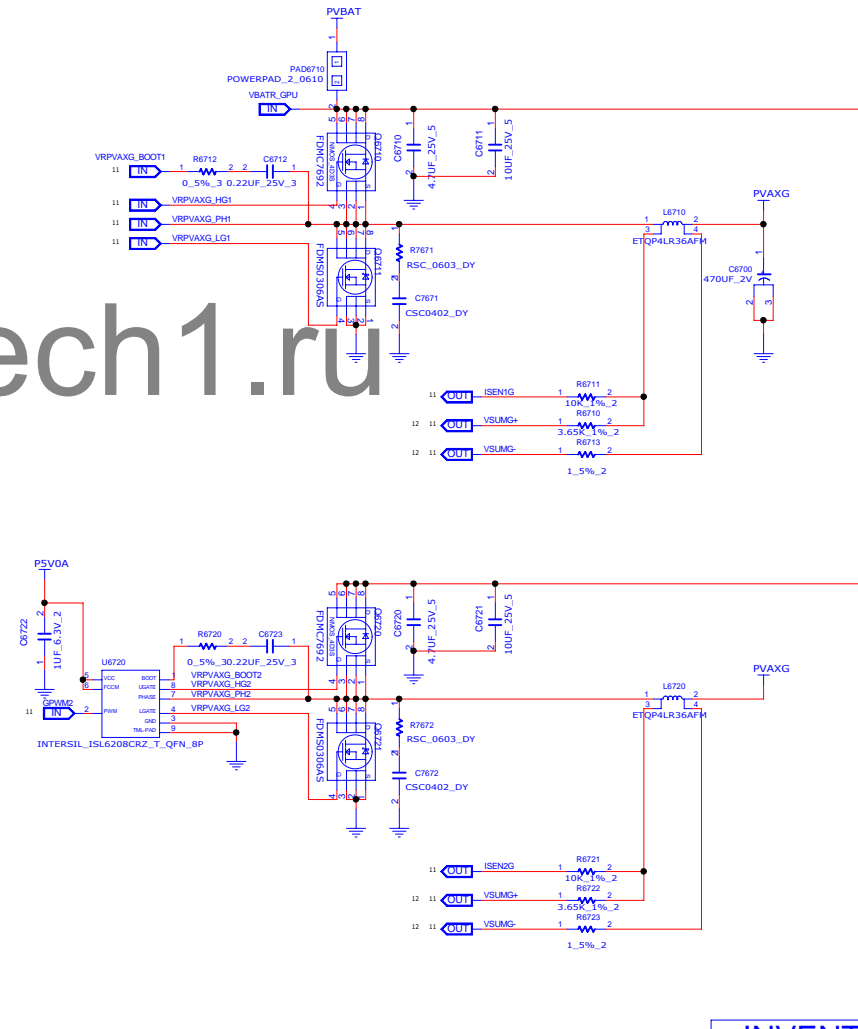
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PVCORE OCP=112A



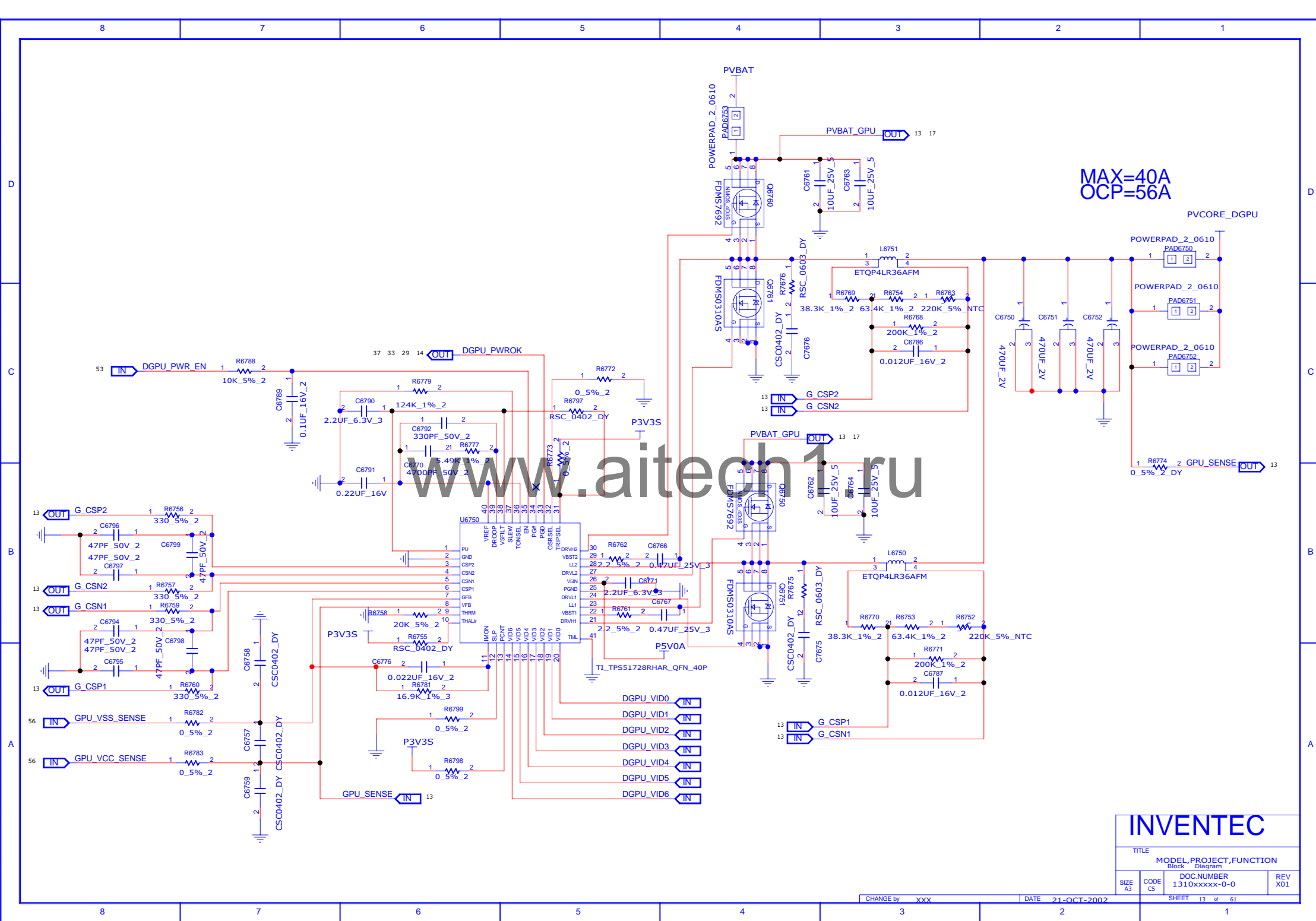
PVAXG OCP=55A

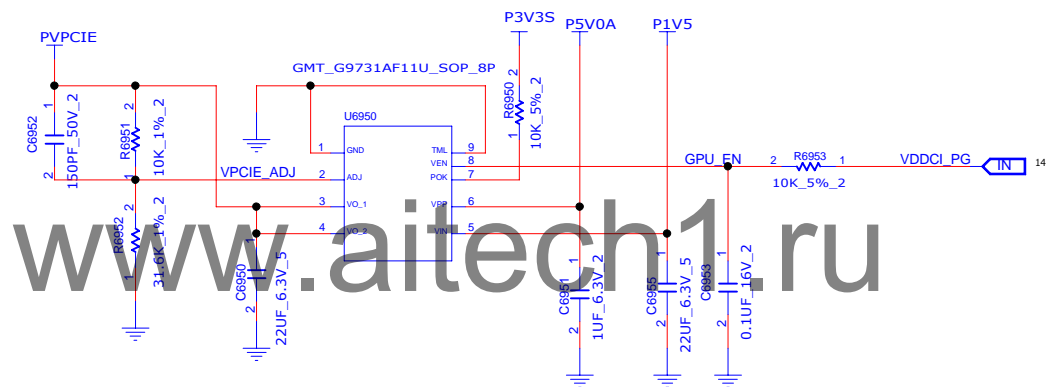


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INVENTEC

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MODEL, PROJECT, FUNCTION			
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SIZE	CODE	DOC NUMBER	REV
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INVENTEC

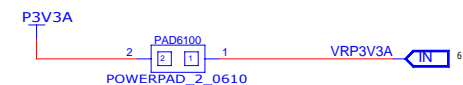
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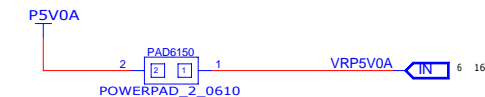
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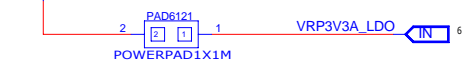
OCP=7AMP



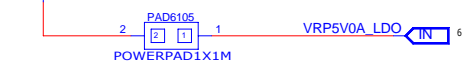
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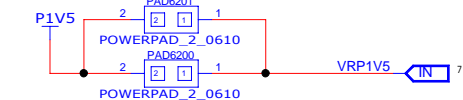
P3V3AL



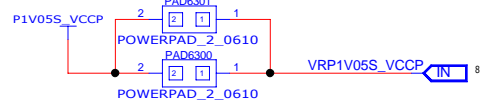
P5V0AL



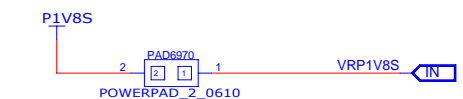
OCP=12AMP



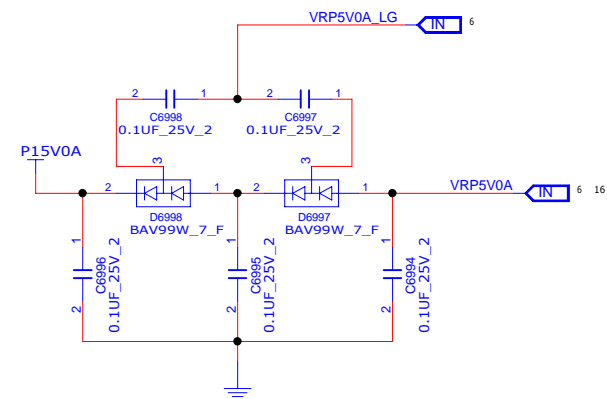
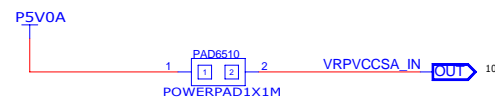
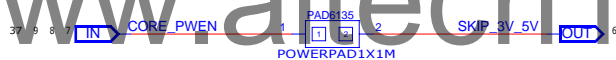
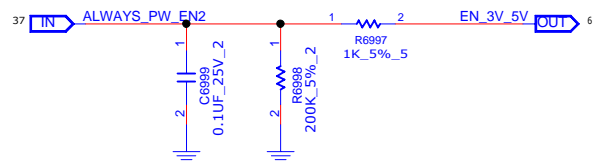
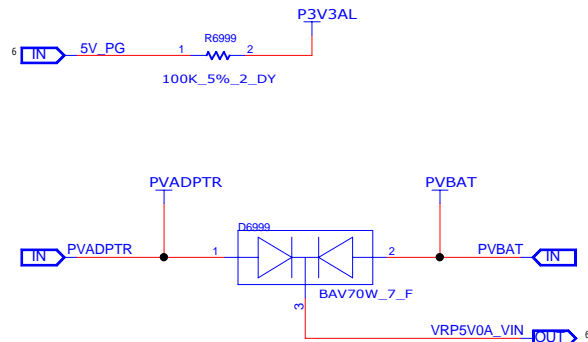
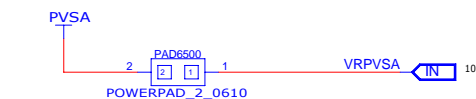
OCP=12AMP



OCP=4.5AMP



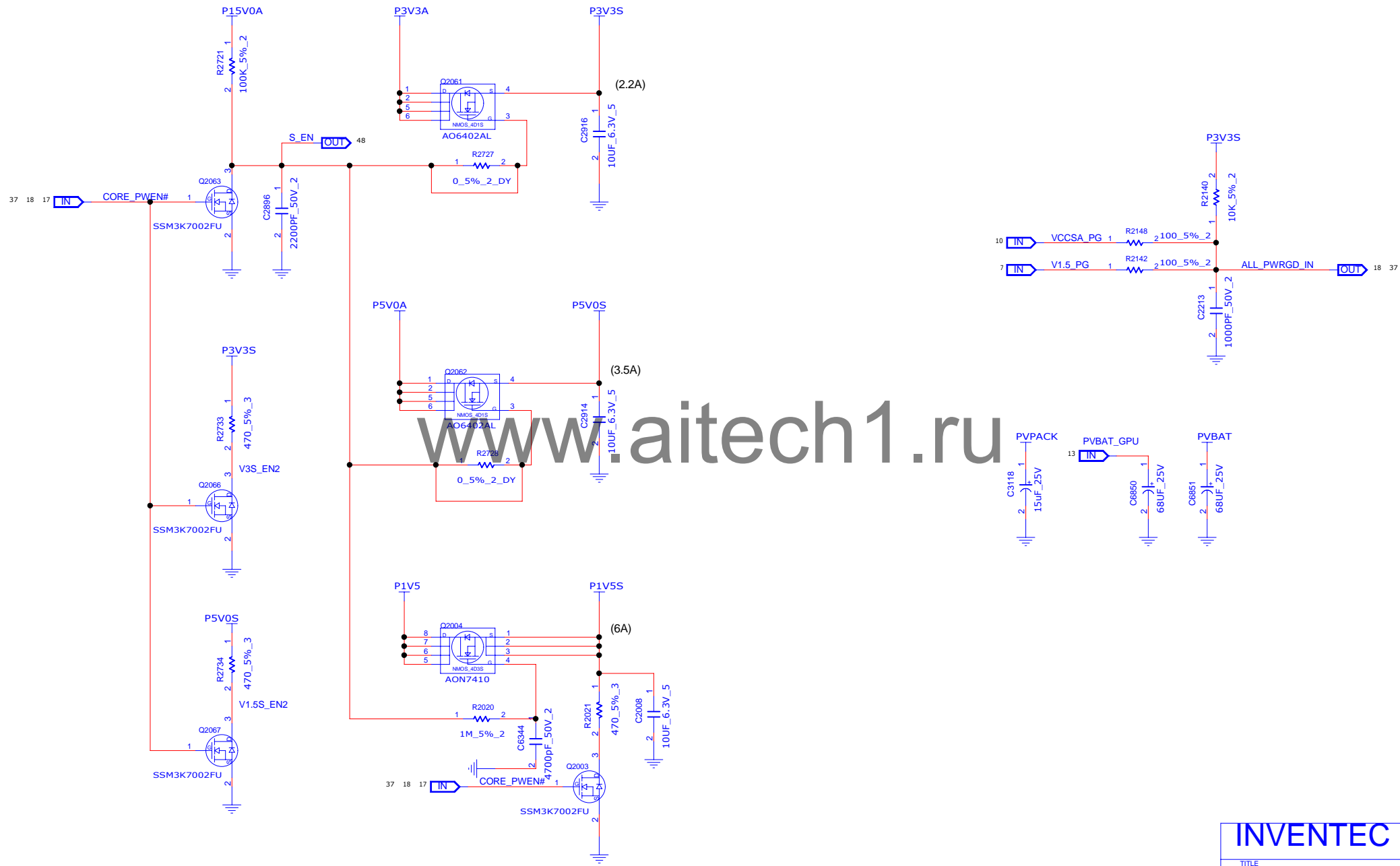
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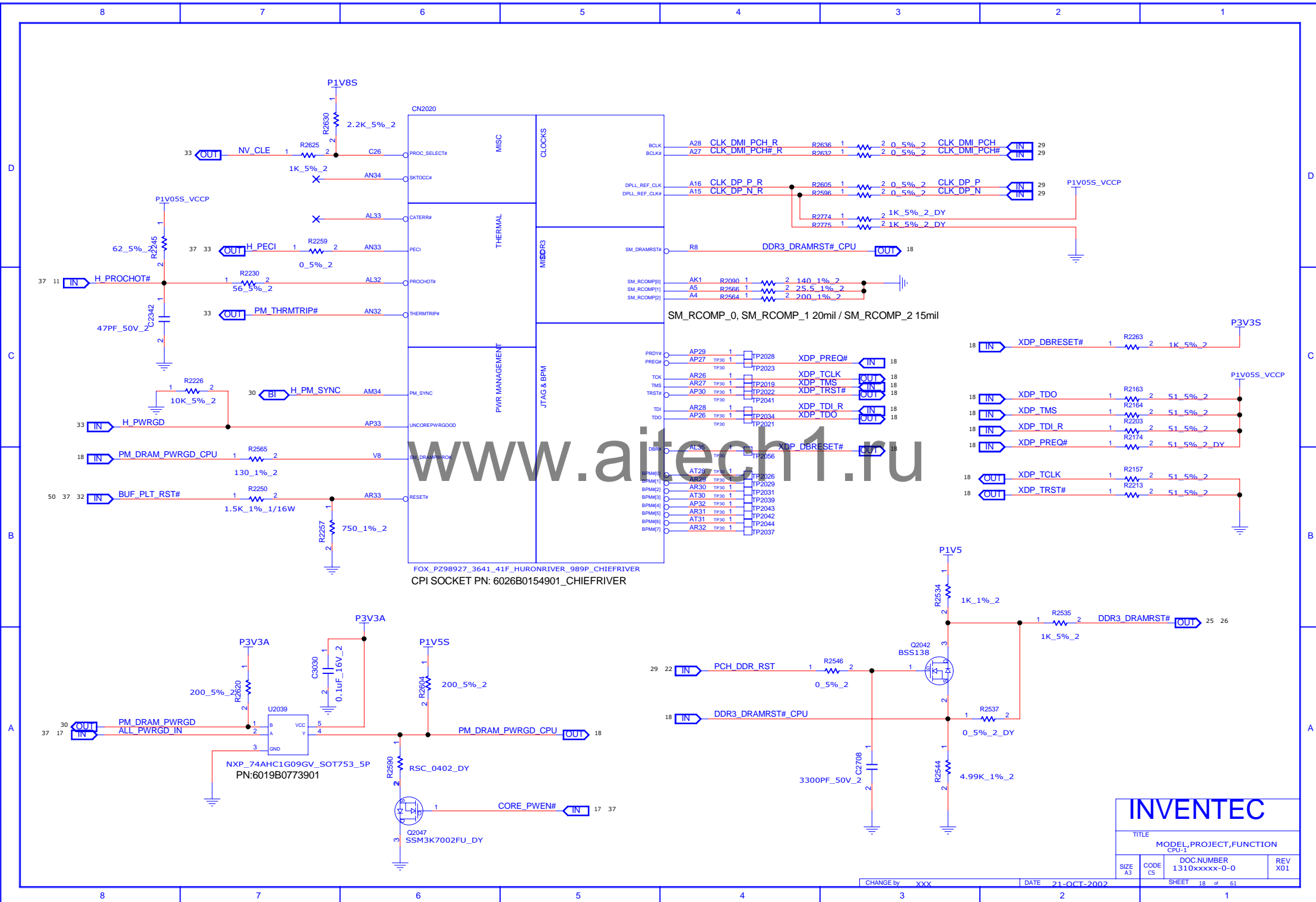
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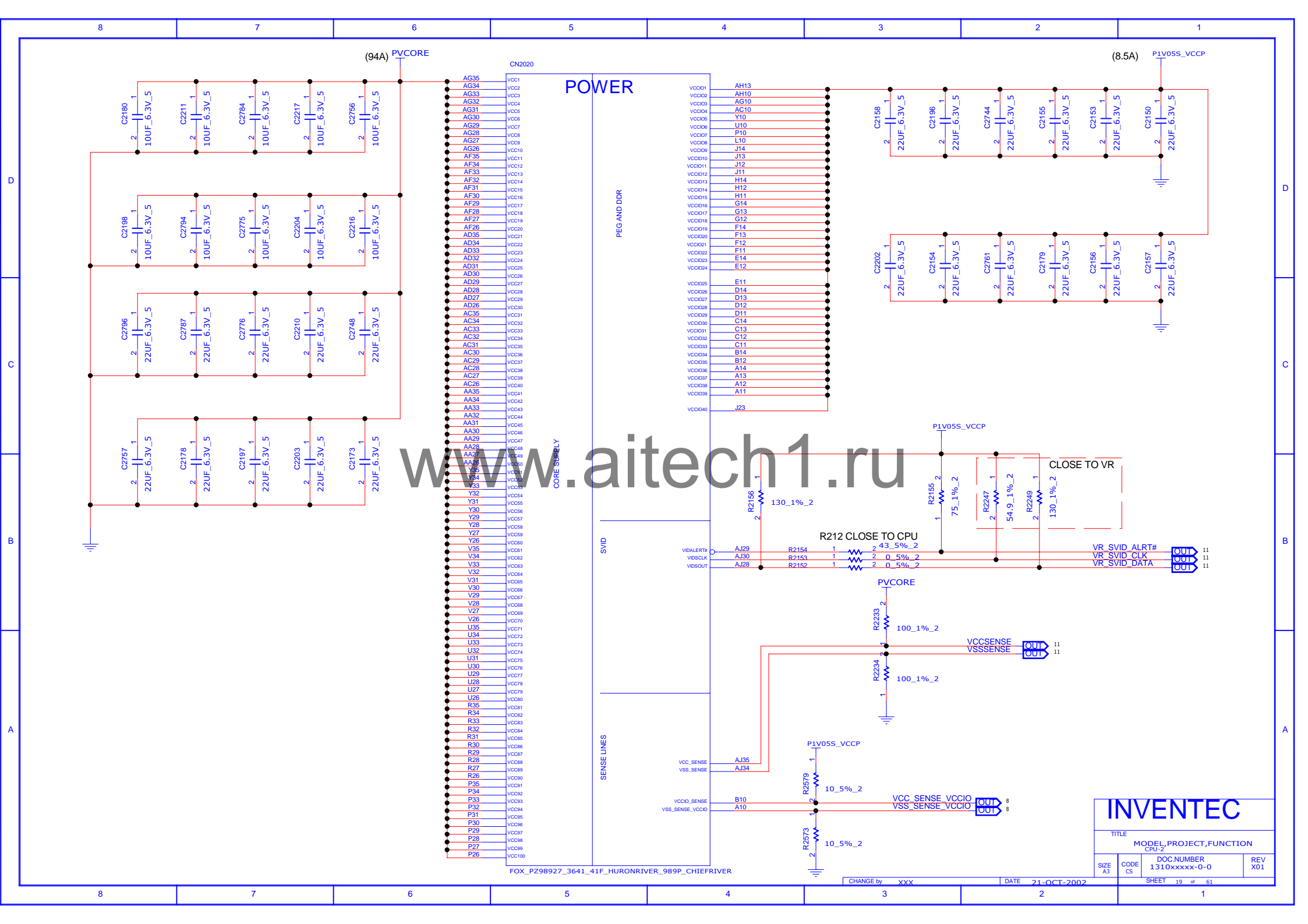
INVENTEC

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MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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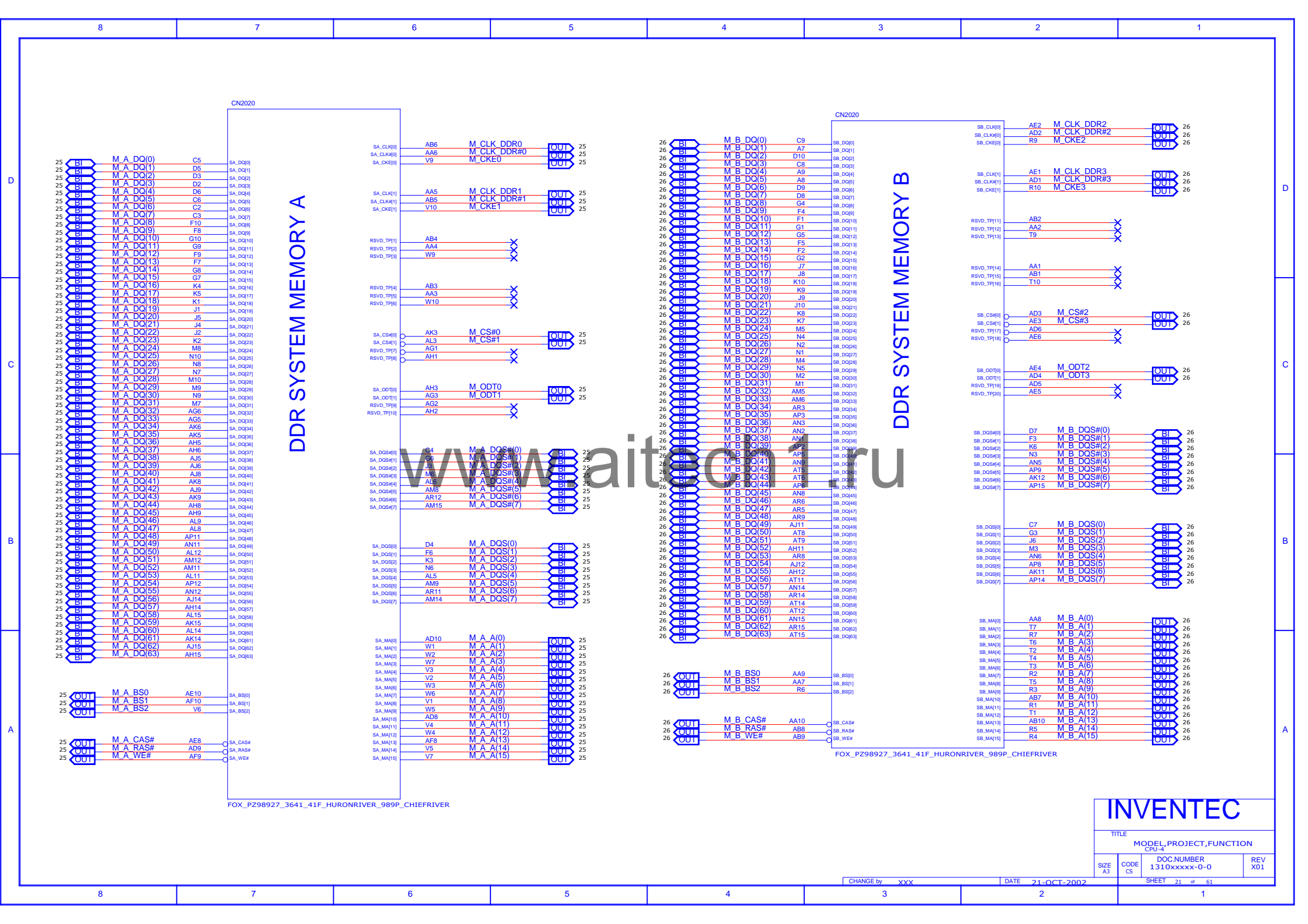
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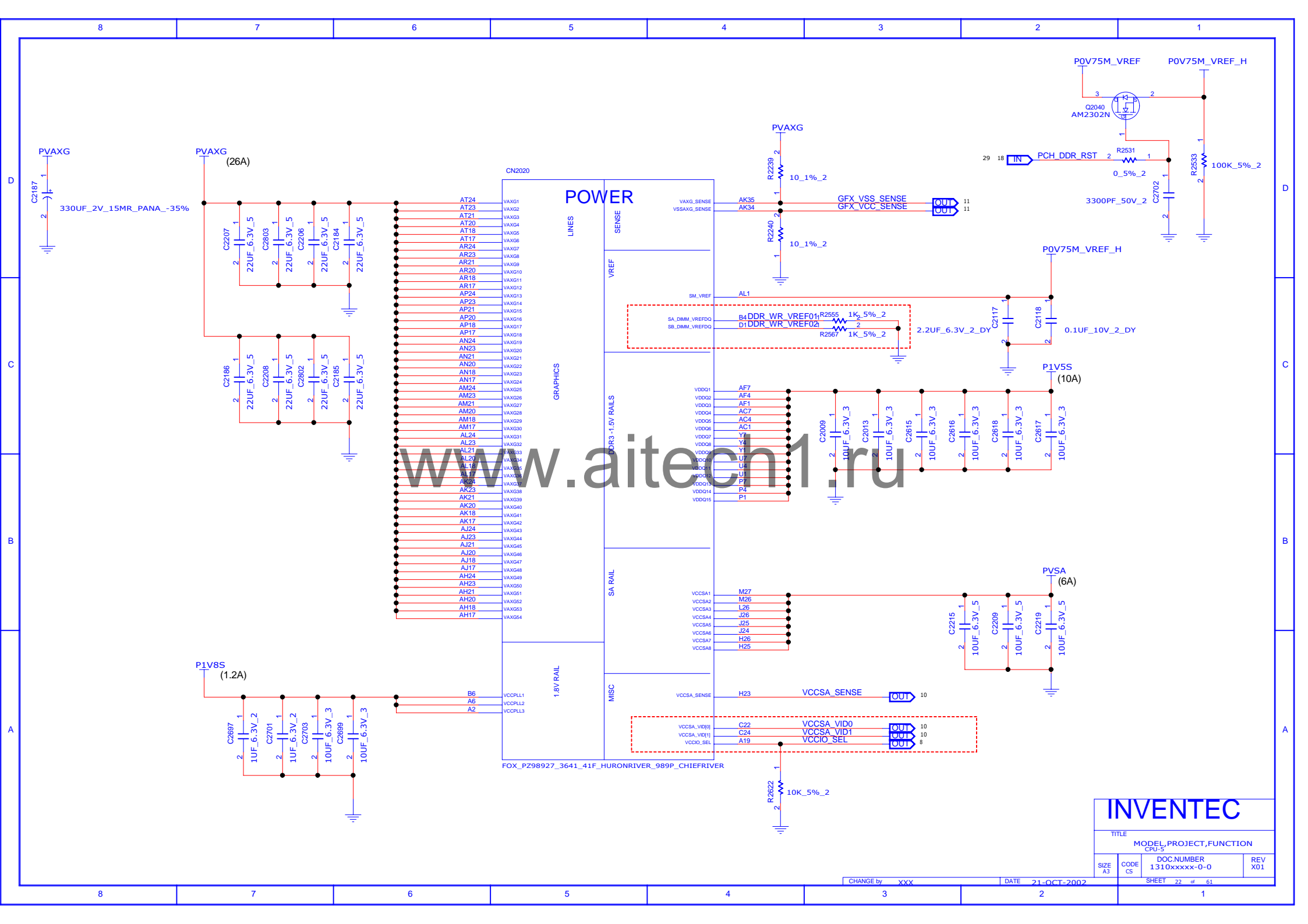




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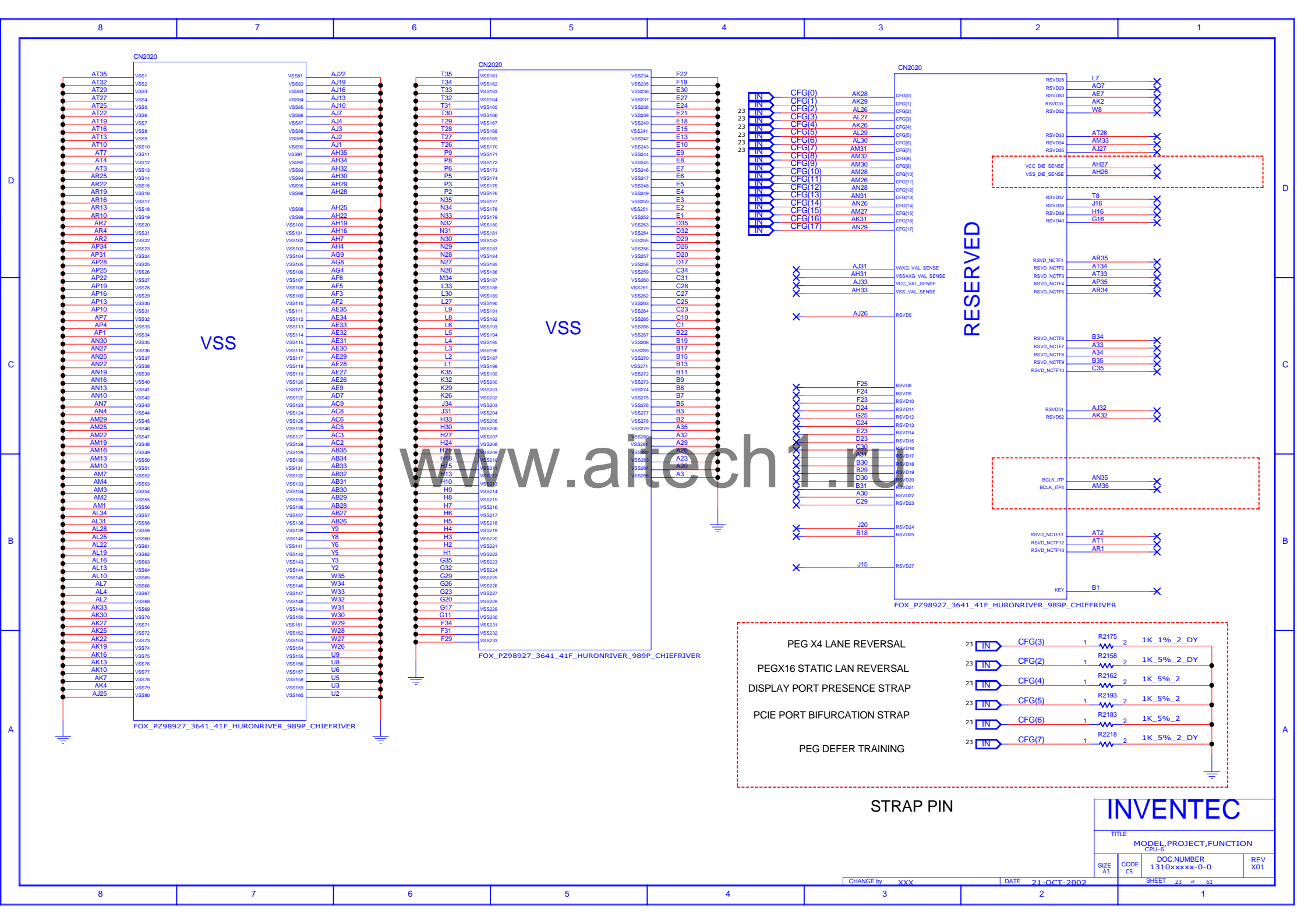
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MODEL PROJECT FUNCTION			
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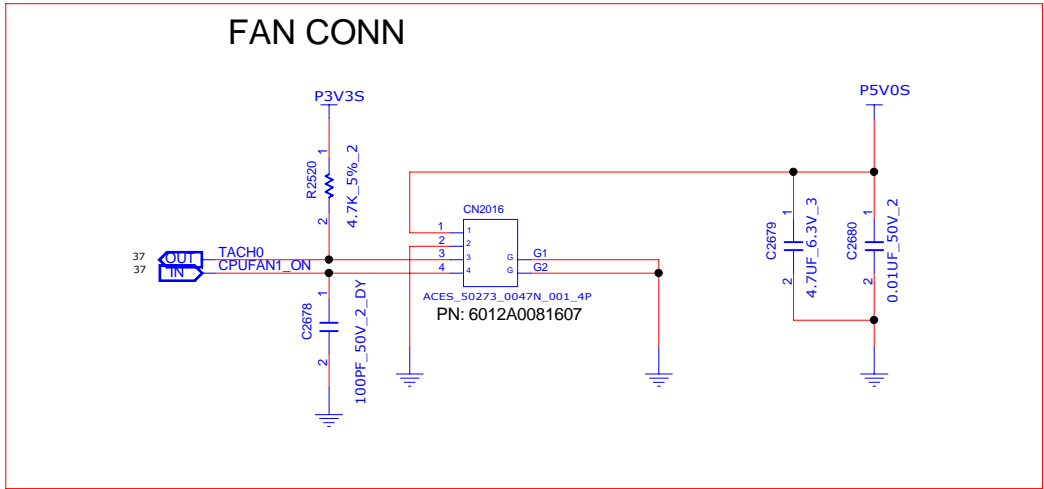




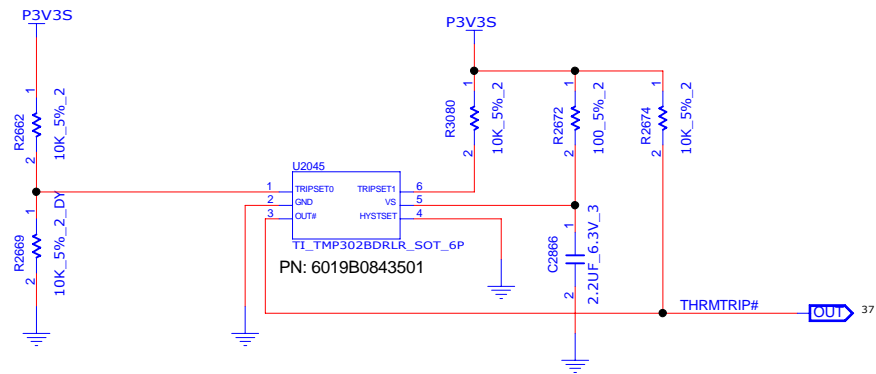
www.aitech1.ru

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
CPU 5			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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INVENTEC

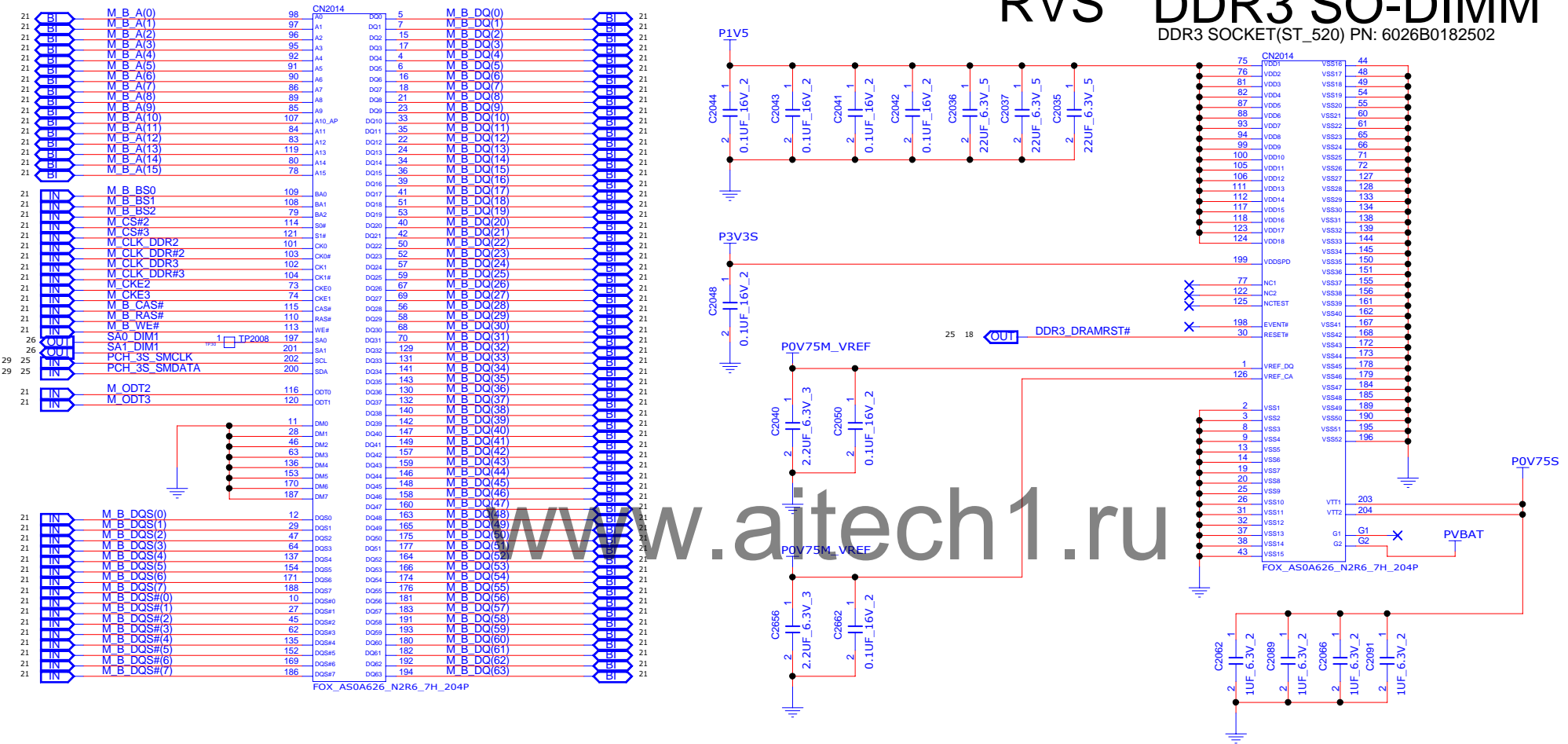
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CHANGE by XXX DATE 21-OCT-2002

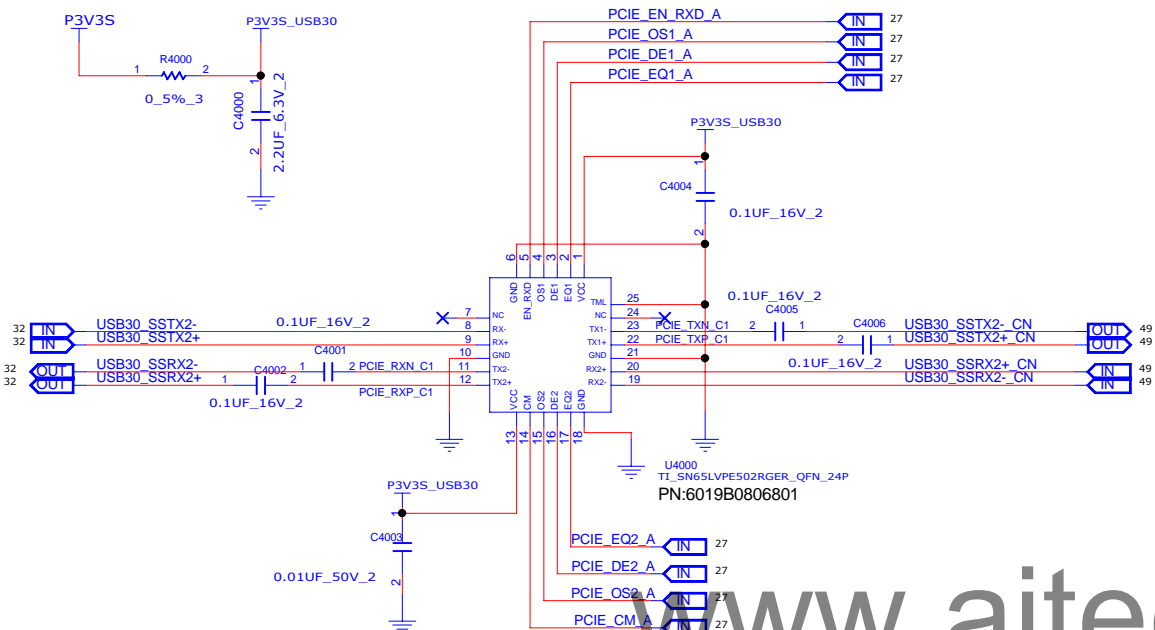
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RVS DDR3 SO-DIMM 1

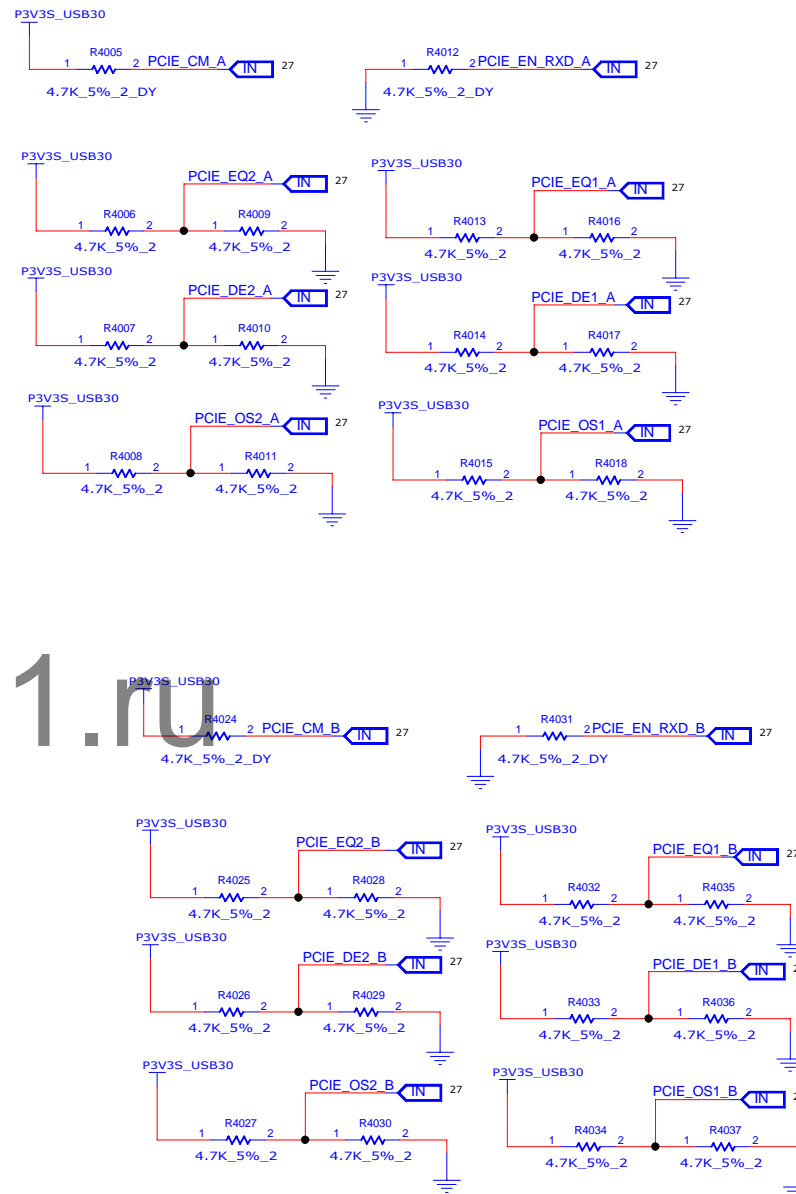
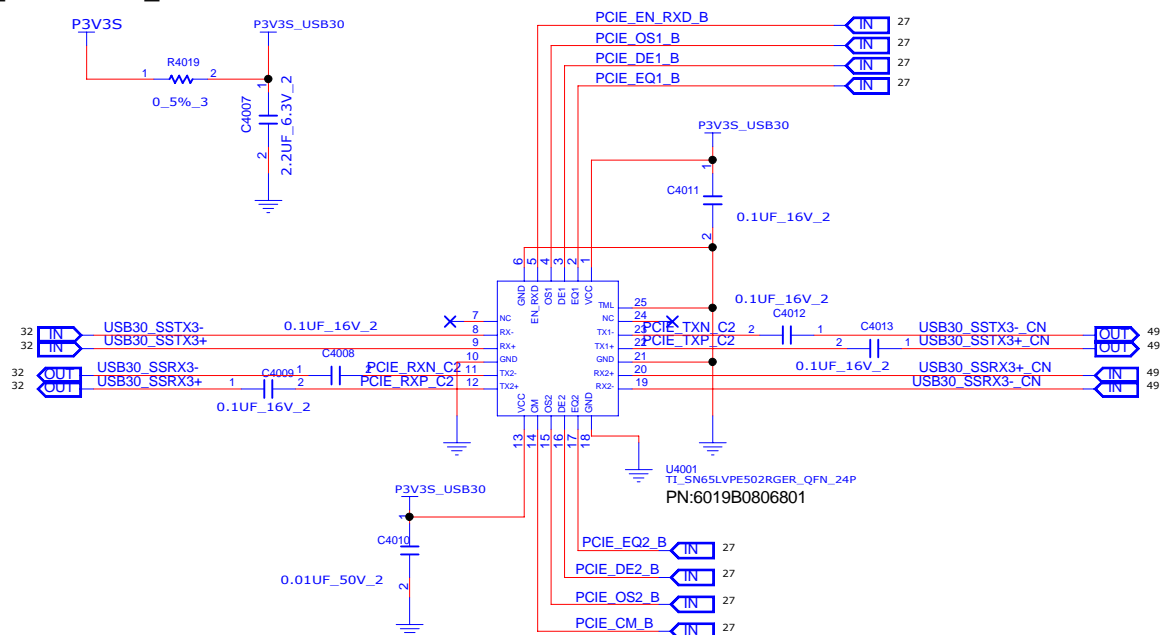
DDR3 SOCKET(ST_520) PN: 6026B0182502



USB30_REDRIIVER_PORT2



USB30_REDRIIVER_PORT3

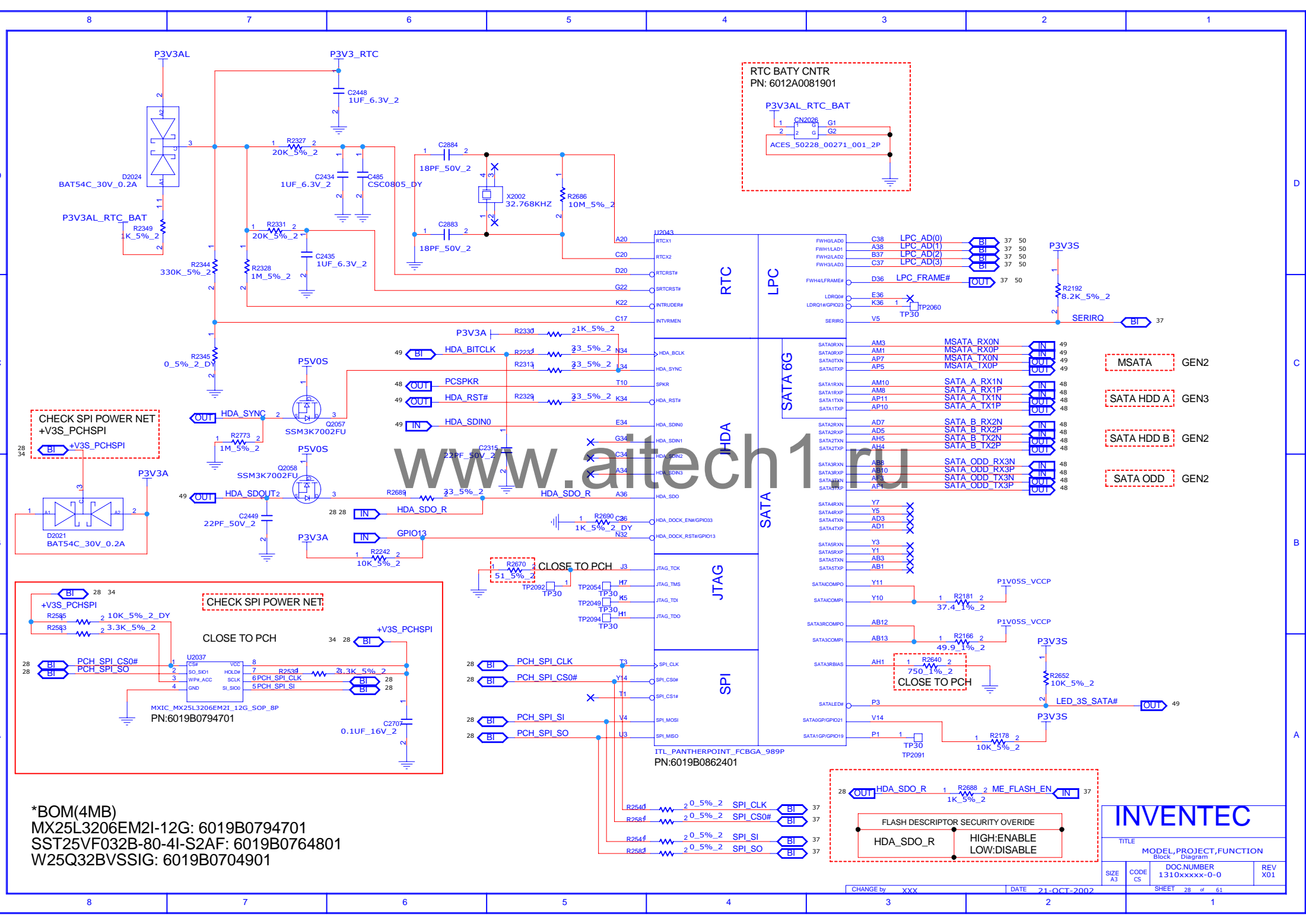


INVENTEC

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MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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RTC BATY CNTR
PN: 6012A0081901

P3V3AL_RTC_BAT

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

ACES_50228_00271_001_2P

CHECK SPI POWER NET
+V3S_PCHSPI

D2021 BAT54C_30V_0.2A

CHECK SPI POWER NET

CLOSE TO PCH

MXIC_MX25L3206EM2I-12G_SOP_BP
PN:6019B0794701

CLOSE TO PCH

J3

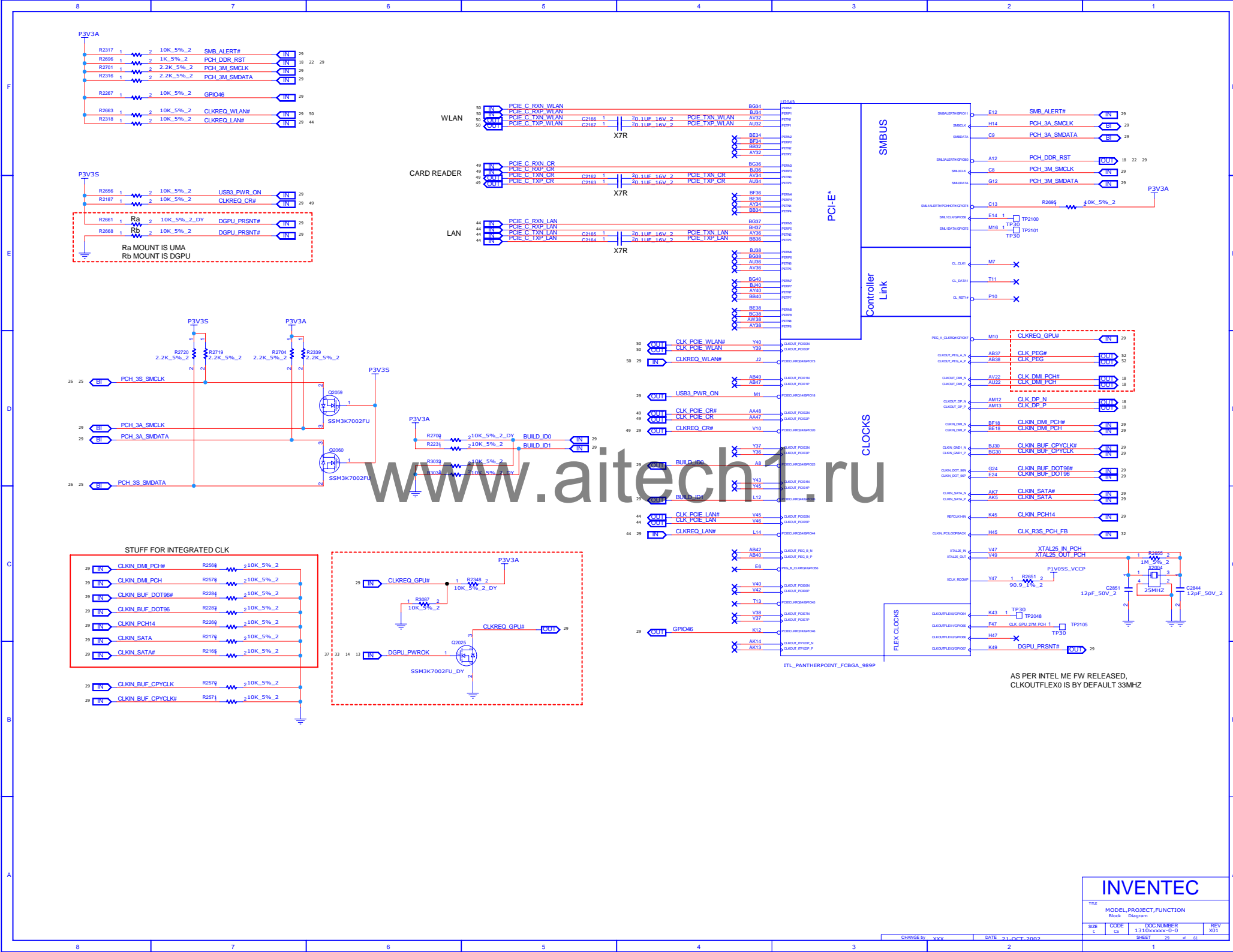
TP2092 TP30 TP2054 TP30 TP2049 TP30 TP2094 TP30

FLASH DESCRIPTOR SECURITY OVERRIDE

HDA_SDO_R HIGH-ENABLE LOW:DISABLE

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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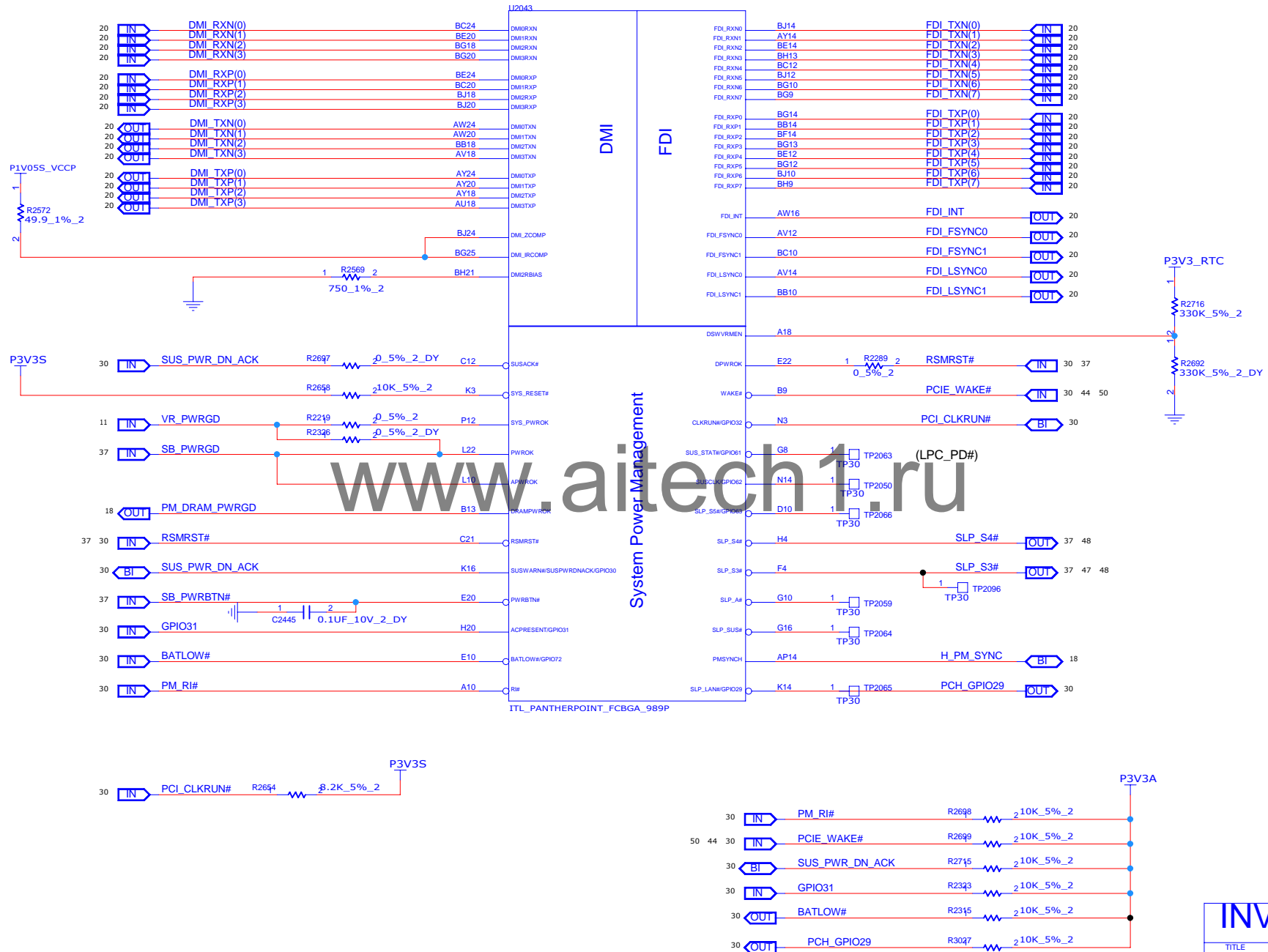
*BOM(4MB)
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SST25VF032B-80-4I-S2AF: 6019B0764801
W25Q32BVSSIG: 6019B0704901



AS PER INTEL ME FW RELEASED.
CLKOUTFLEX0 IS BY DEFAULT 33MHZ

INVENTEC			
MODEL/PROJECT/FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310XANX00-0-0	X01
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NOTE:1.SLP_SUS AND SUSACK# ARE NC IF DSW IS NOT SUPPORTED
2.DPWROK SHOULD CONNECT TO RSMRST# IF DSW NOT SUPPORTED



INVENTEC

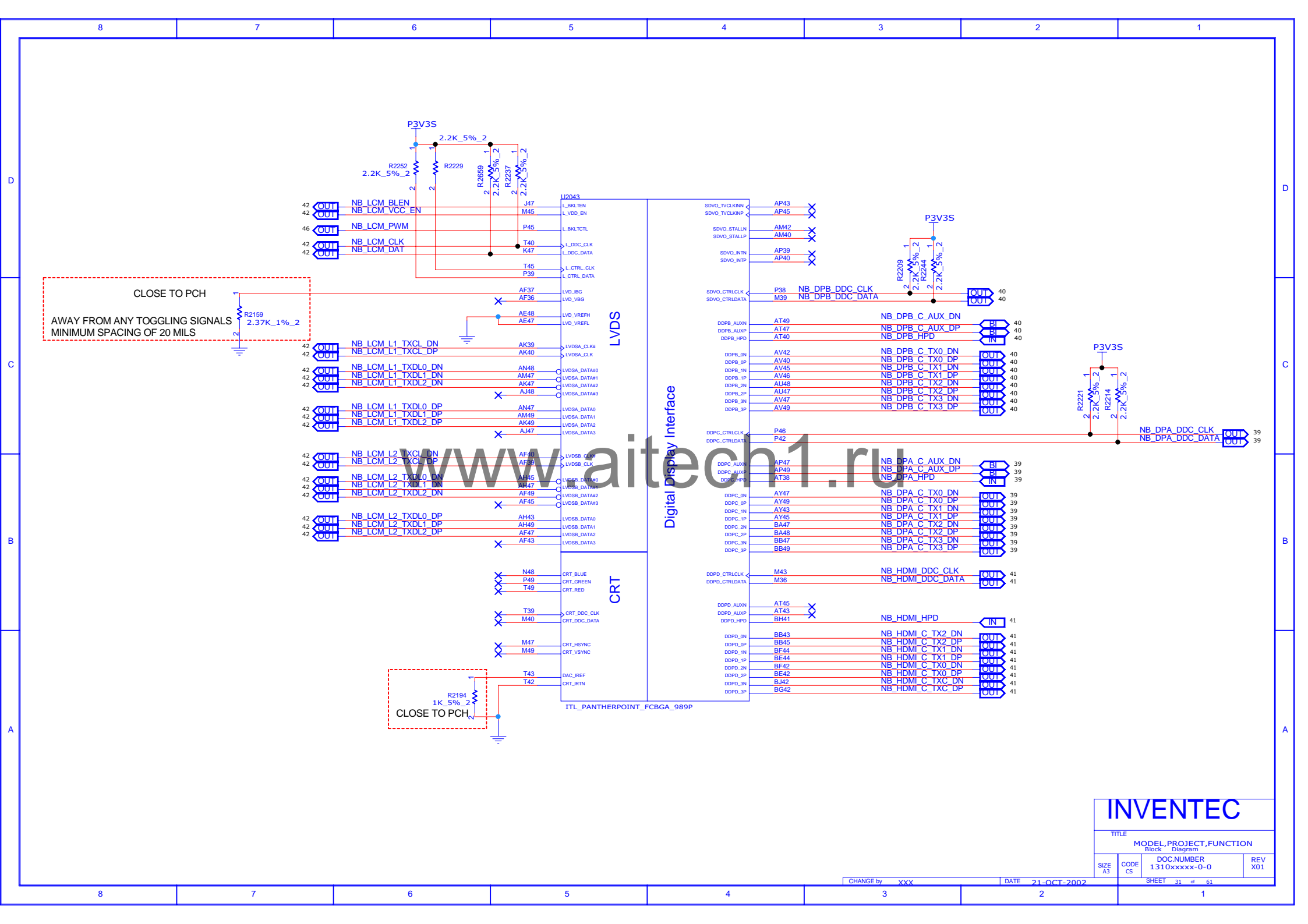
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SIZE A3	CODE C5	DOC.NUMBER 1310xxxxx-0-0	REV X01
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INVENTEC

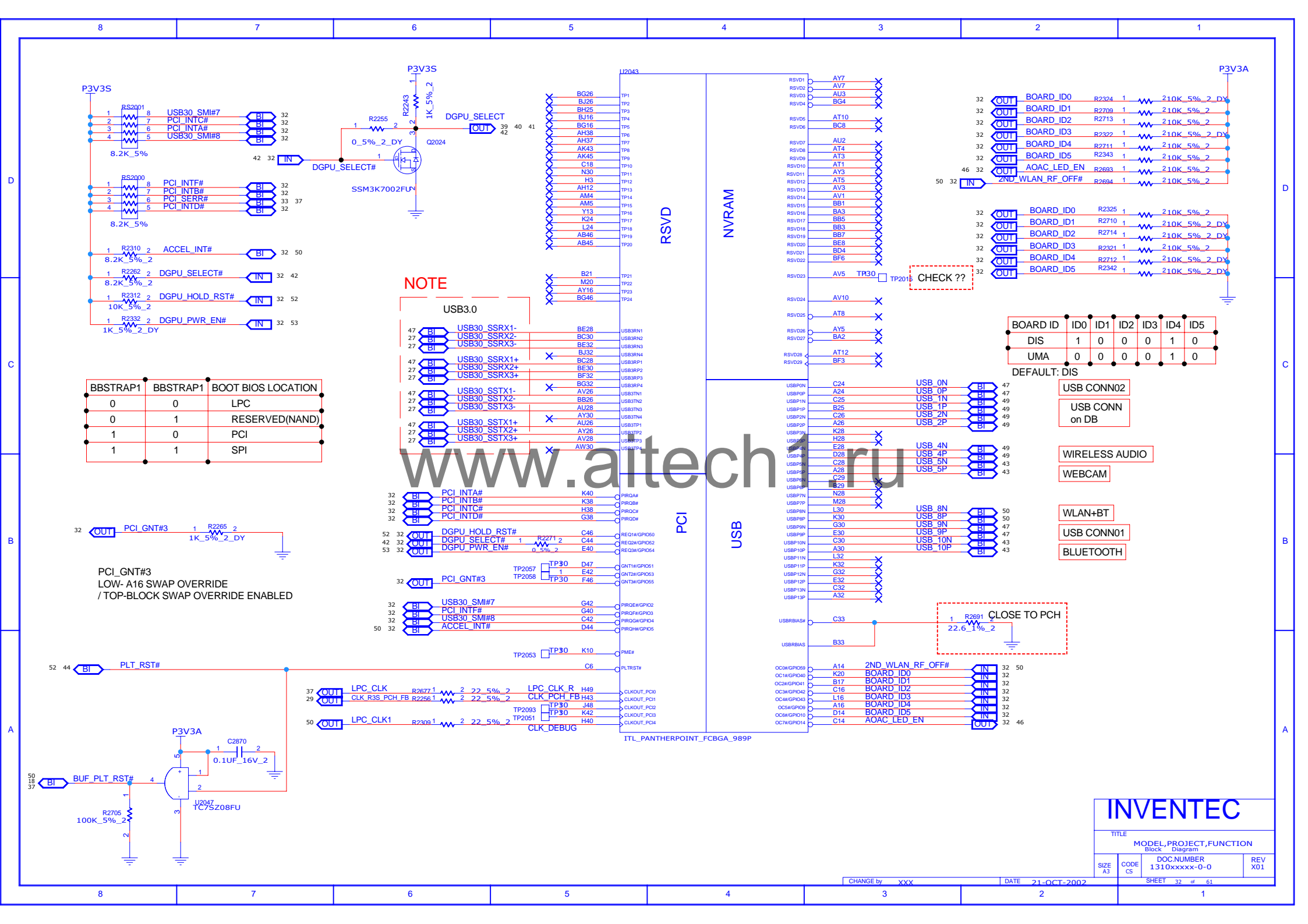
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DOC NUMBER
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REV
X01

CHANGE by XXX DATE 21-OCT-2002

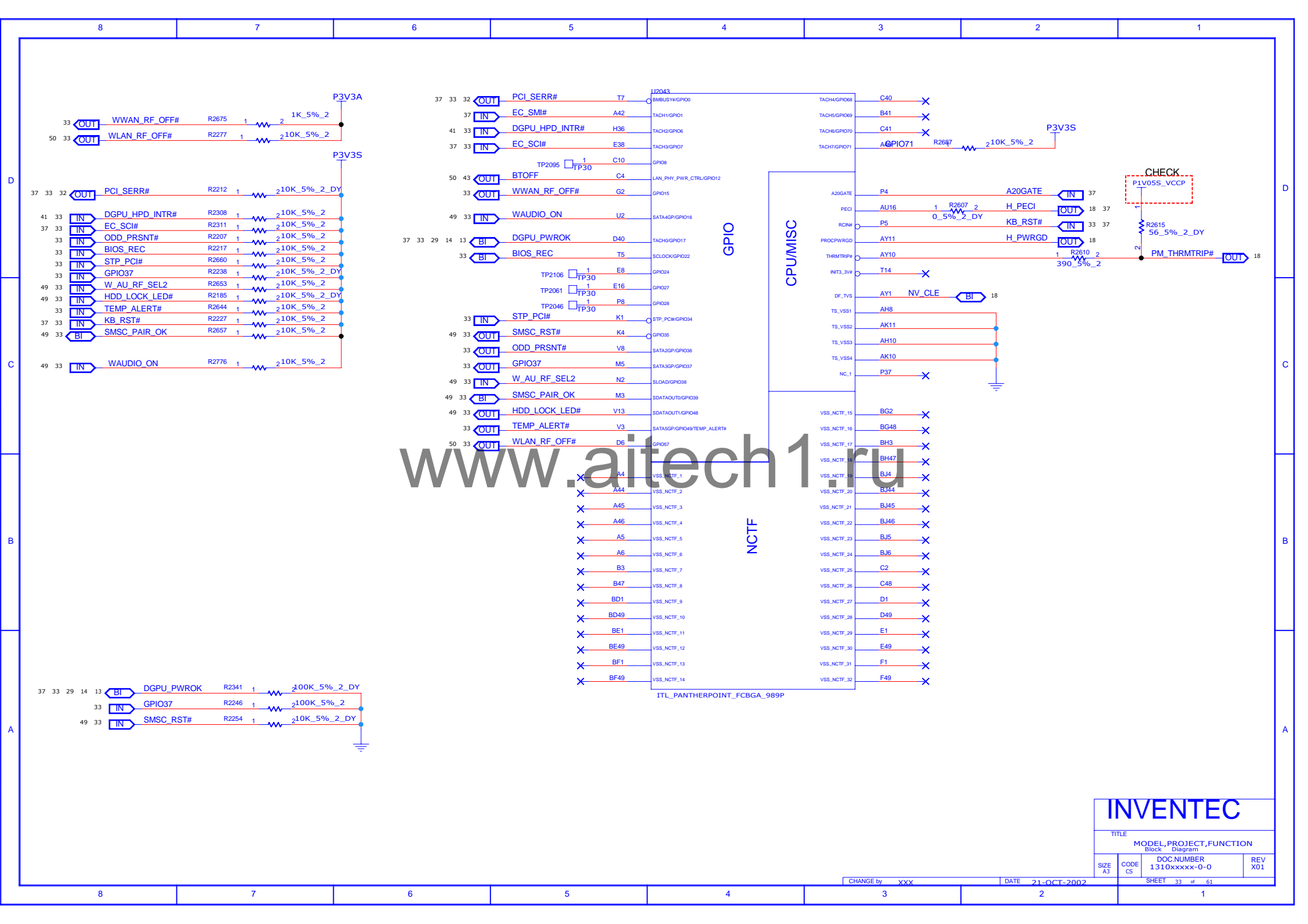
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NOTE

USB3.0





INVENTEC

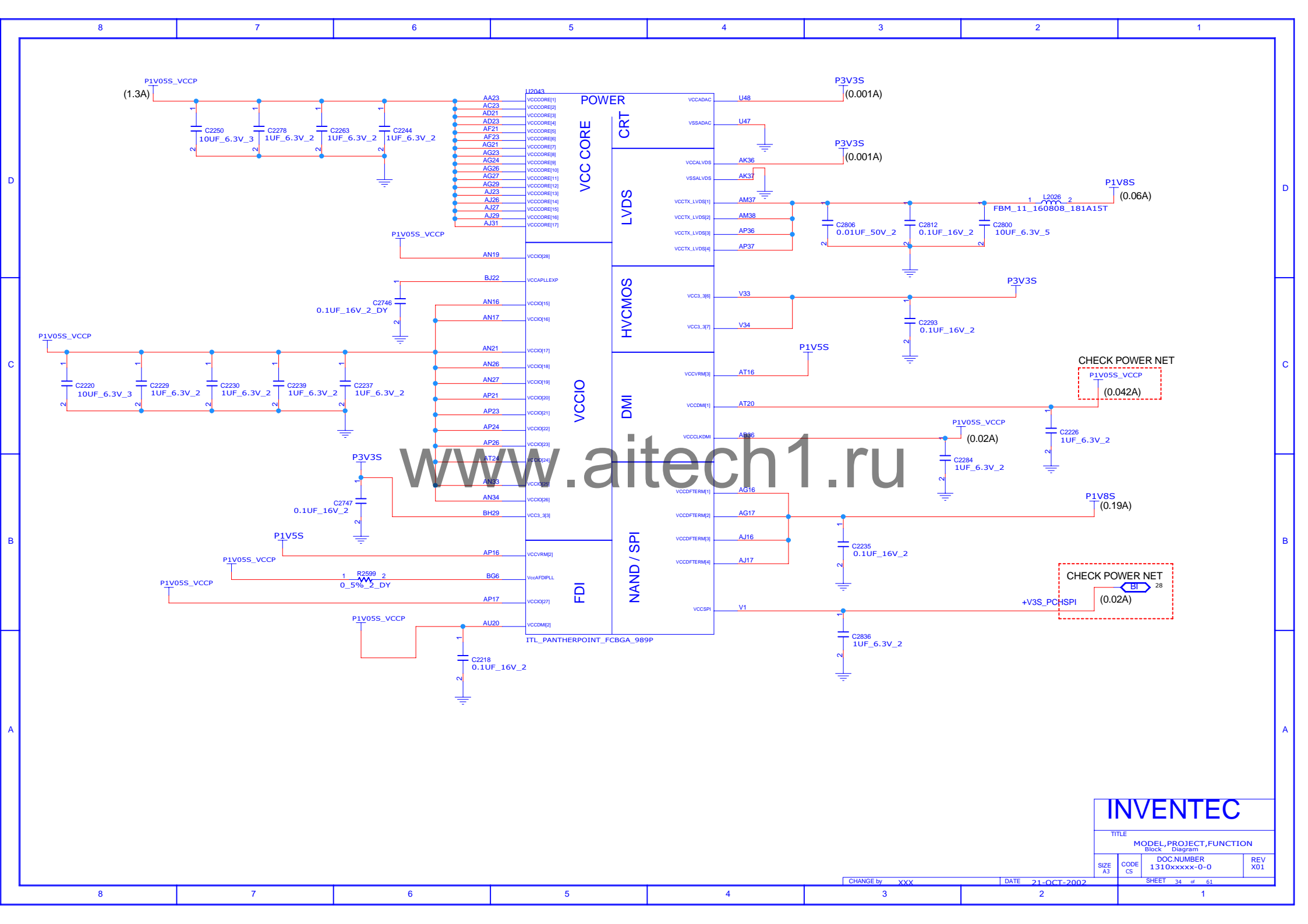
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SIZE CODE DOC NUMBER REV

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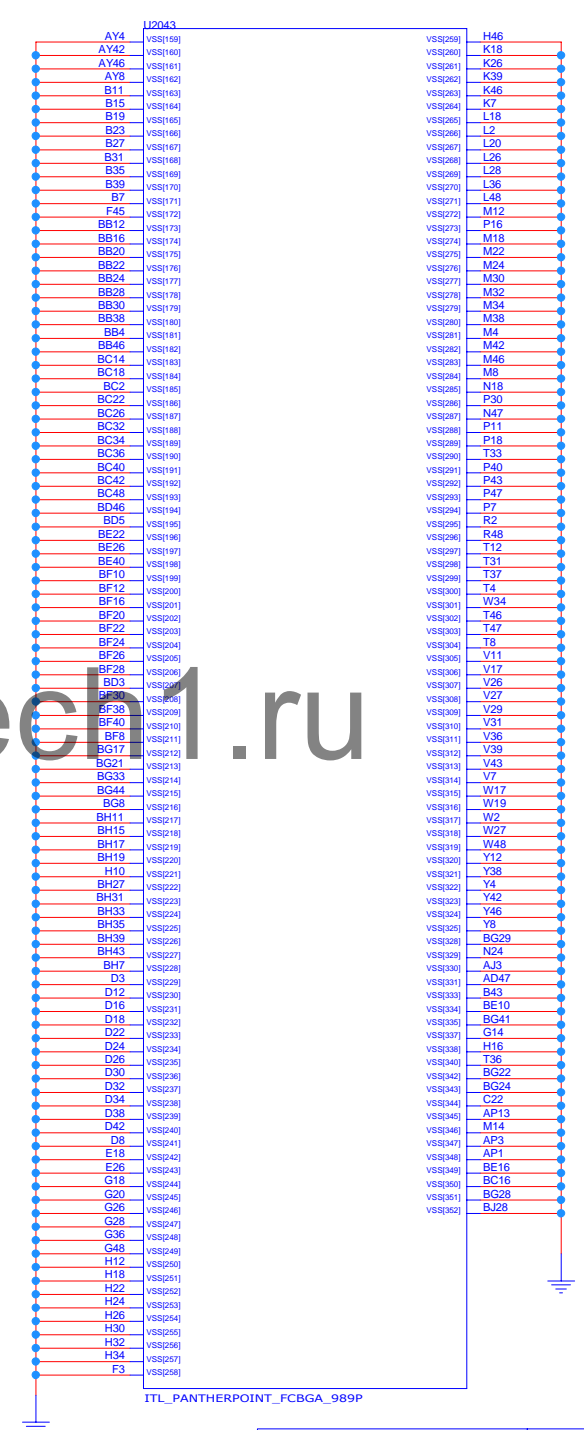
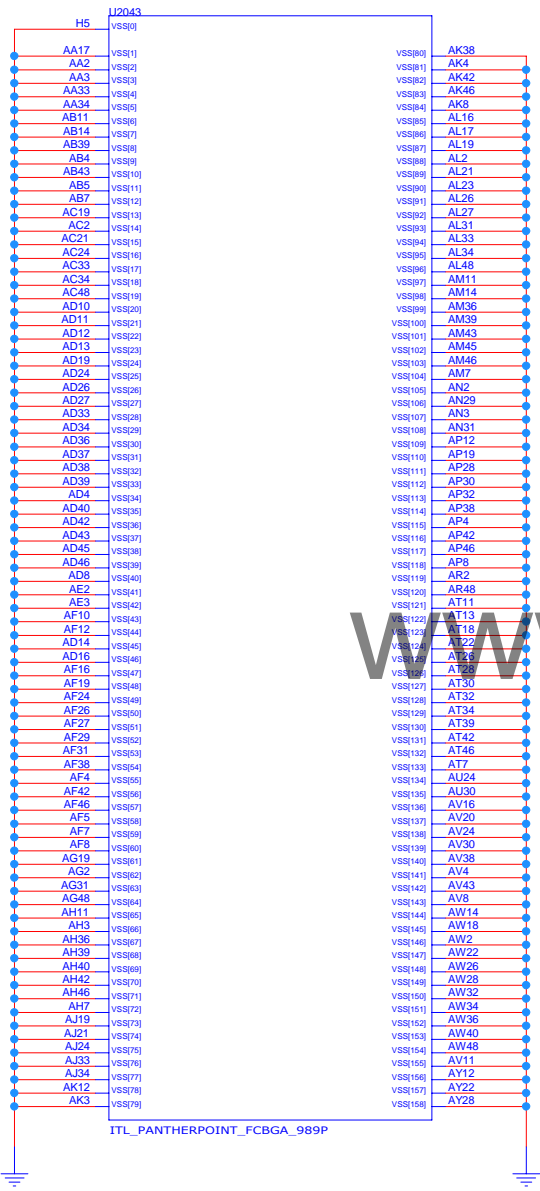
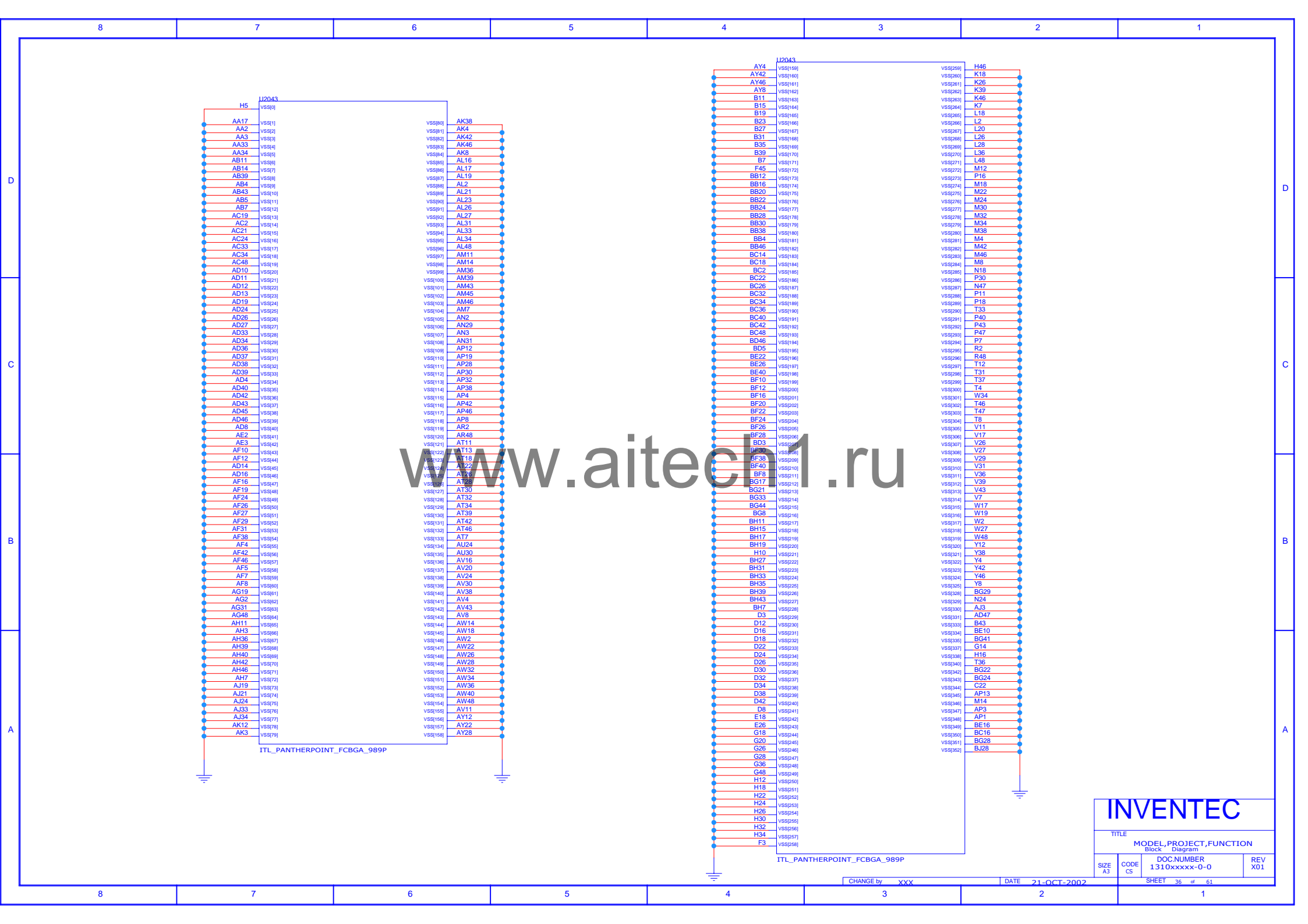
CHANGE by XXX DATE 21-OCT-2002

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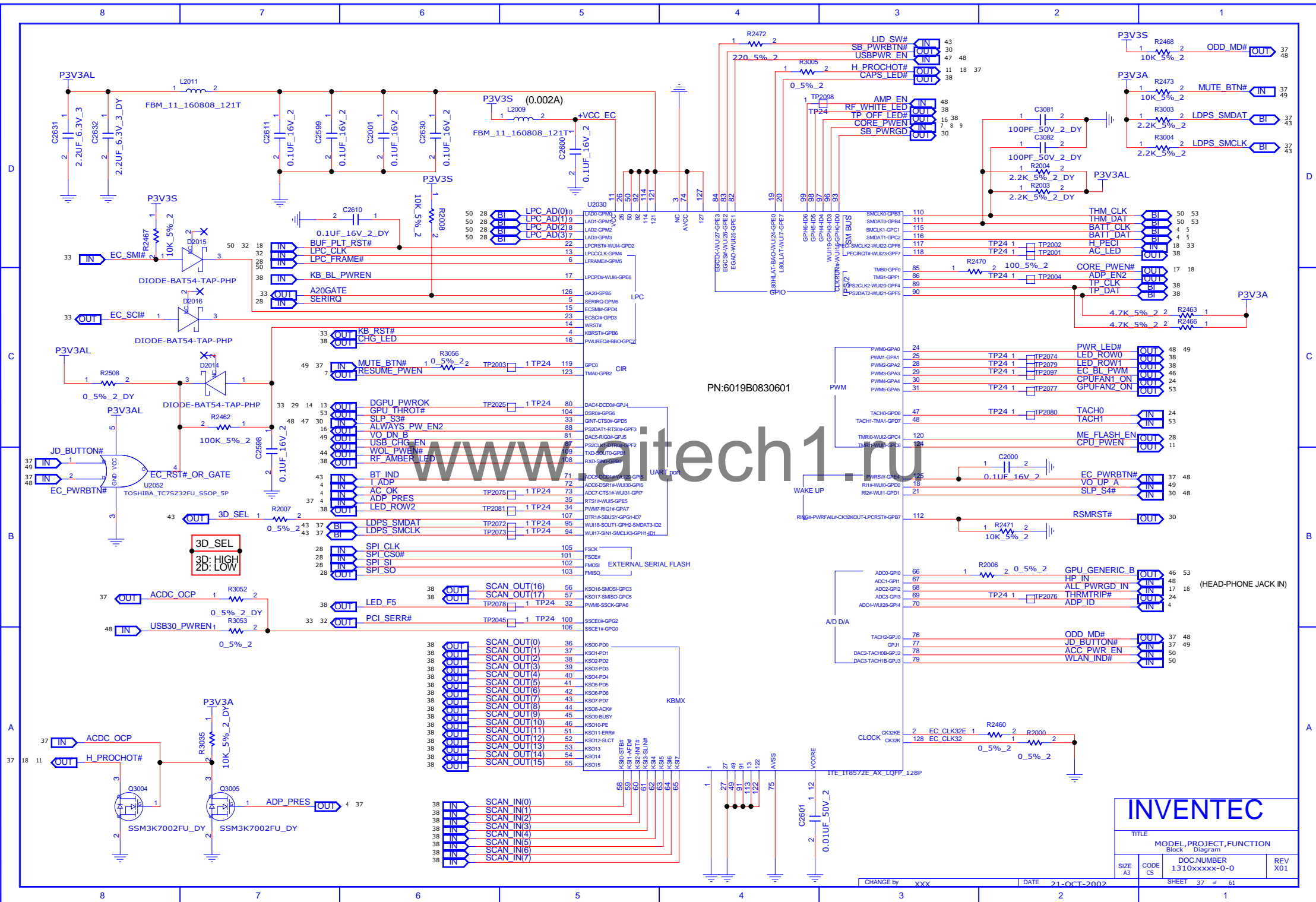


www.aitech1.ru

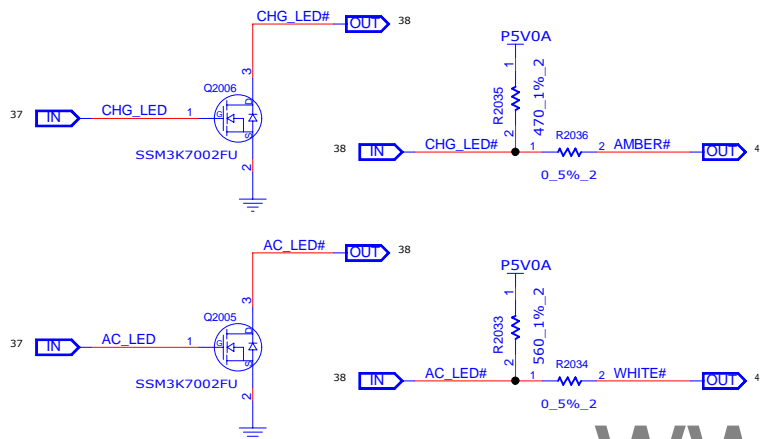
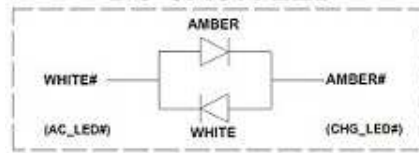
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TITLE				
MODEL,PROJECT,FUNCTION				
BLOCK				
DOC NUMBER				
1310xxxxx-0-0				
REV				
X01				



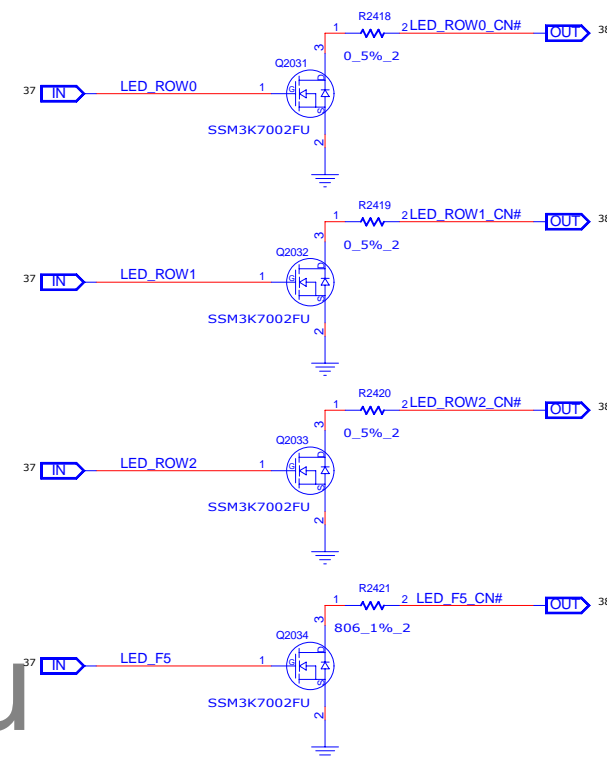
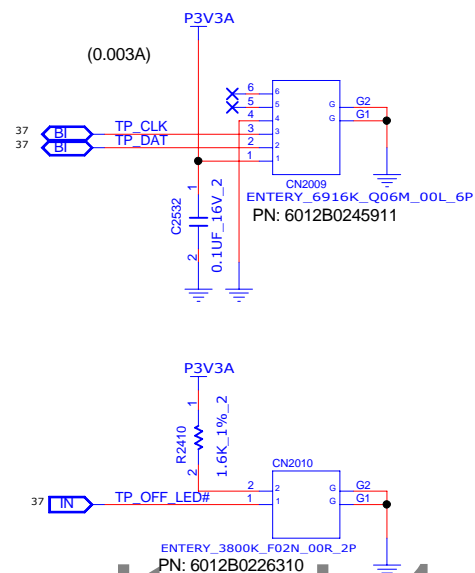
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TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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DC-JACK LED

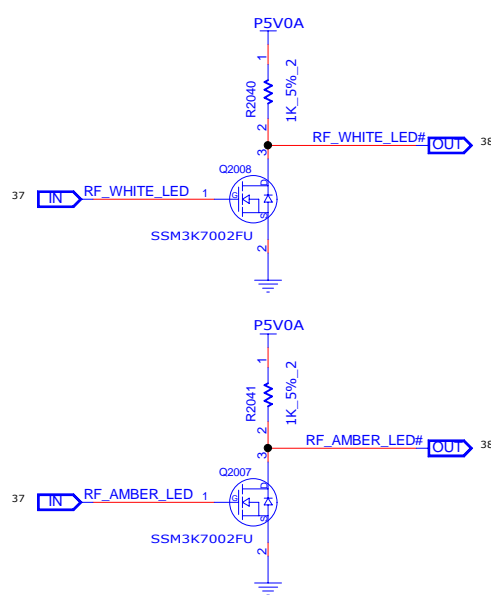
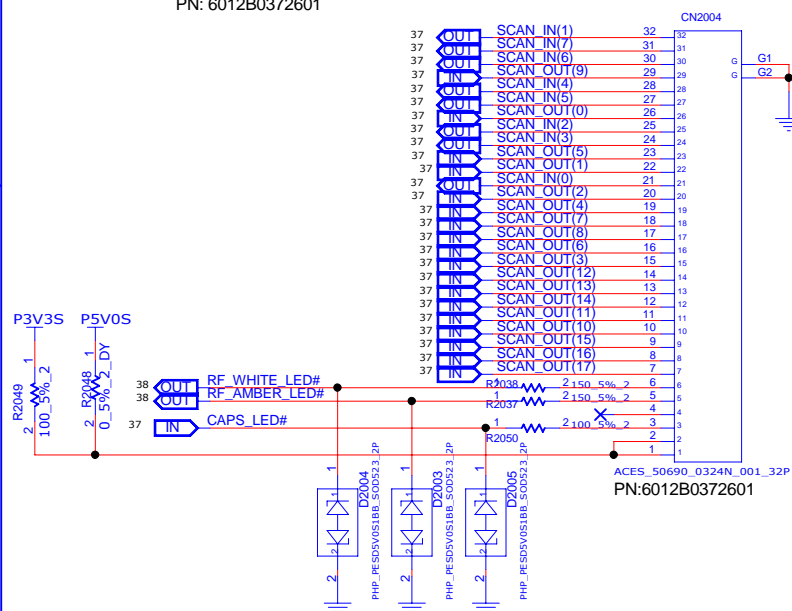


TOUCHPAD MODULE CONN

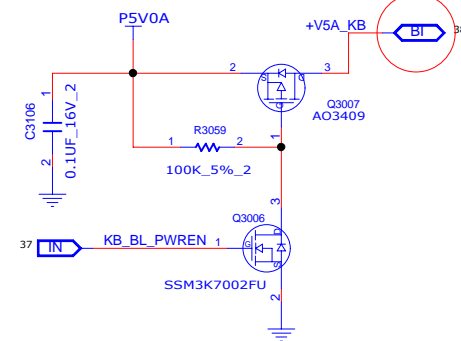
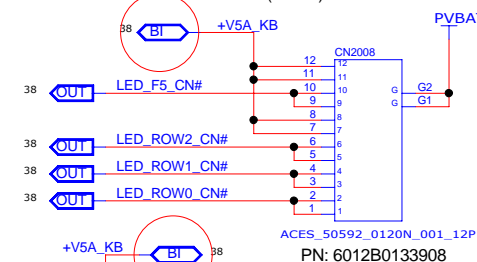


KEYBOARD CONN (32 PIN)

PN: 6012B0372601



BACK LIGHT LED CONN (12PIN)



INVENTEC

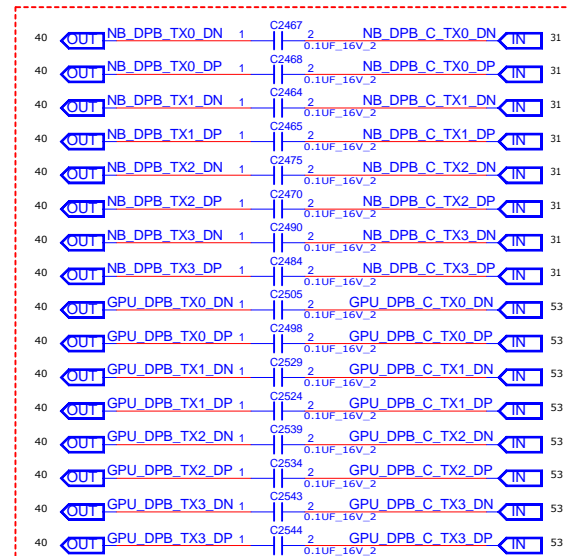
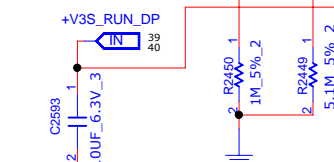
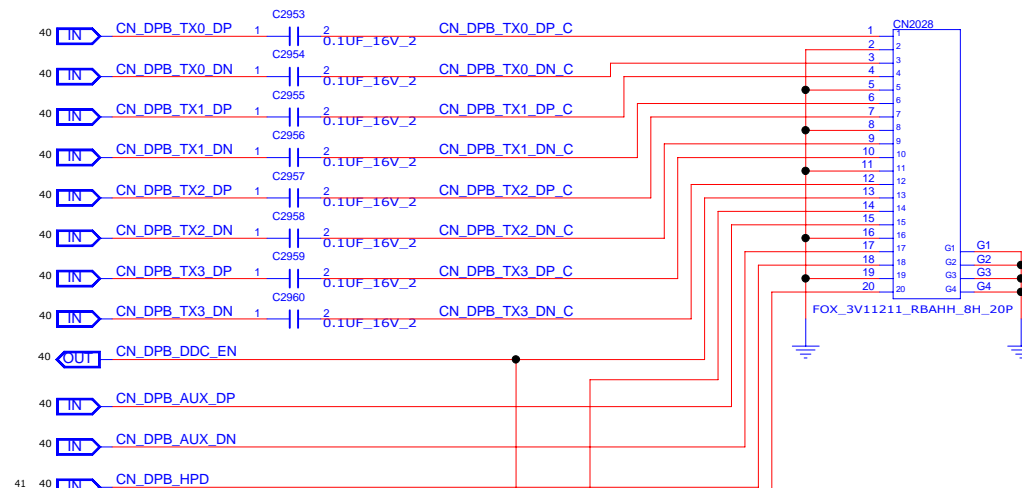
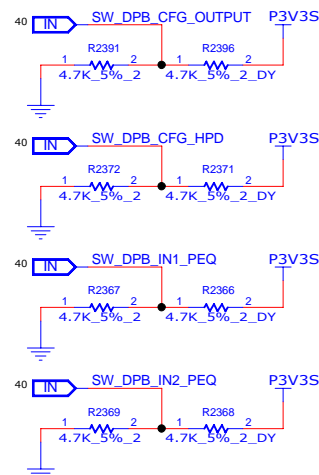
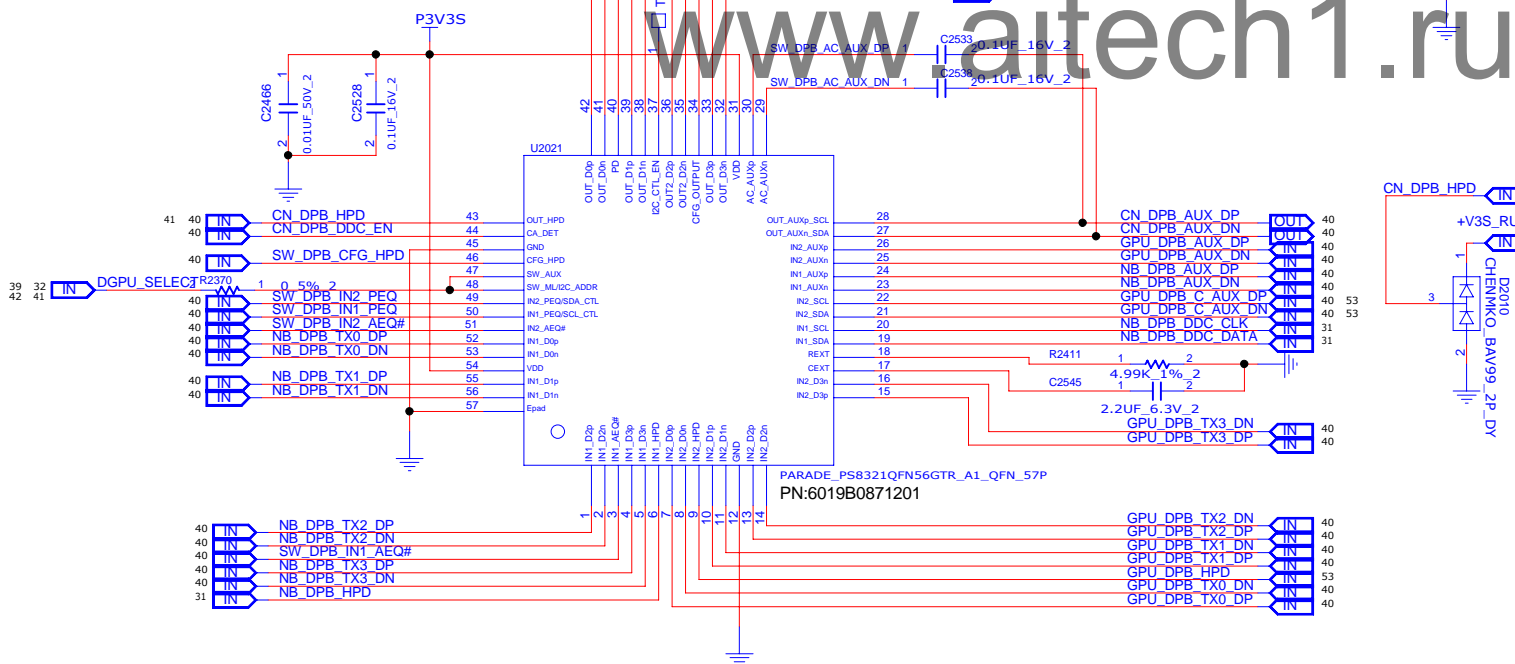
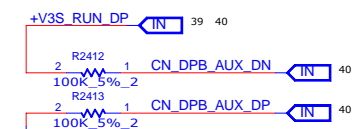
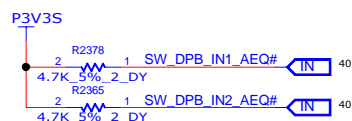
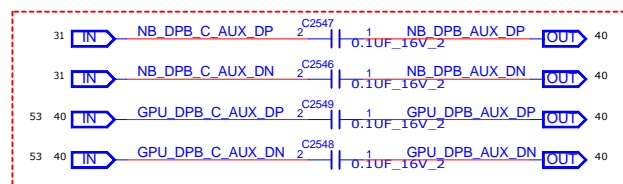
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

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Display Port B



DGPU_SELECT
H : IN2 PORT IS SELECTED (GPU)
L : IN1 PORT IS SELECTED (NB)

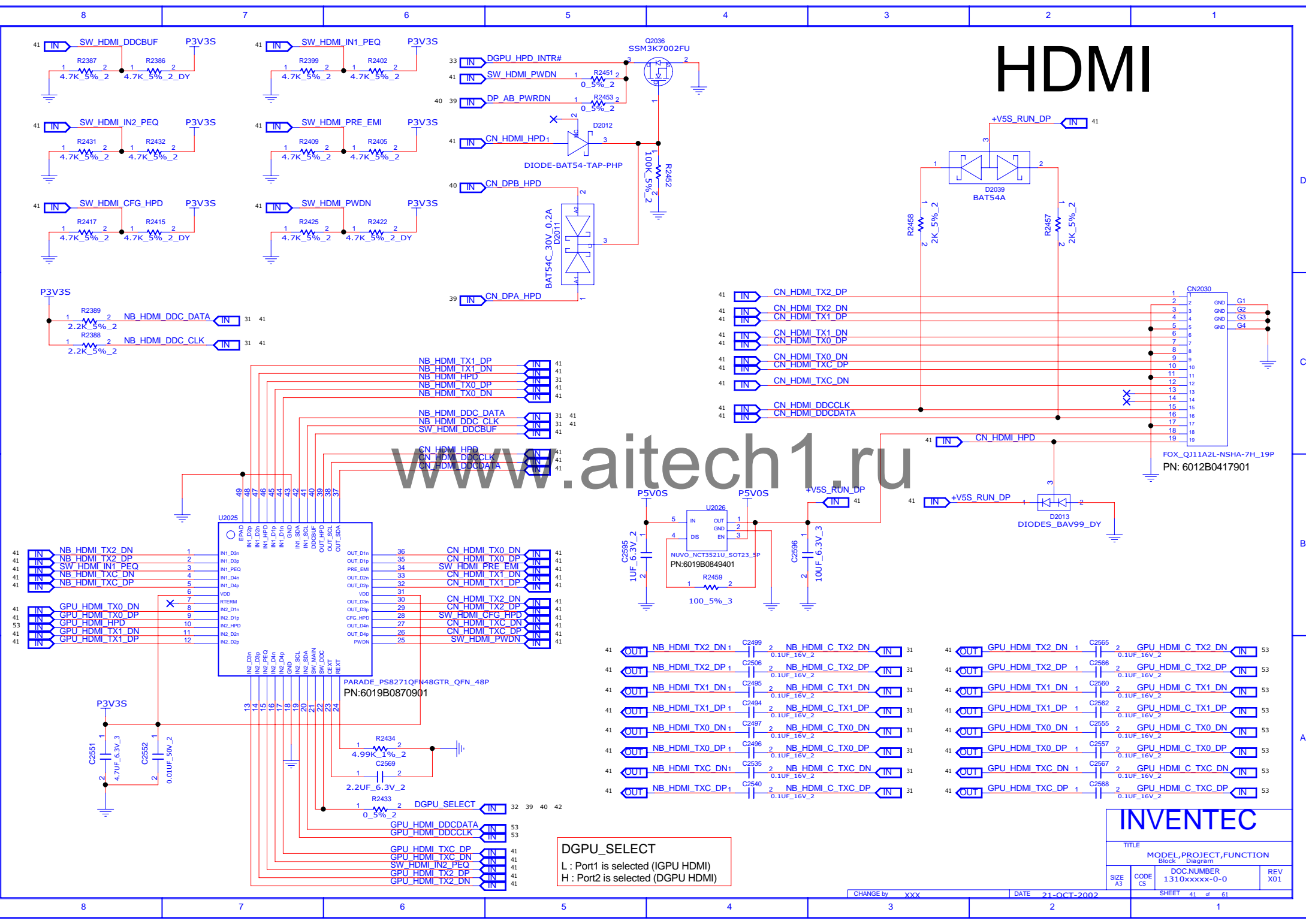
SW_ML=L , PORT1	CA_DET=L, AUX
SW_AUX=L , PORT1	CA_DET=H, I2C
SW_ML=H , PORT2	
SW_AUX=H , PORT2	

INVENTEC

TITLE				MODEL,PROJECT,FUNCTION			
				Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0				R X	
SHEET 10 OF 20							

HDMI

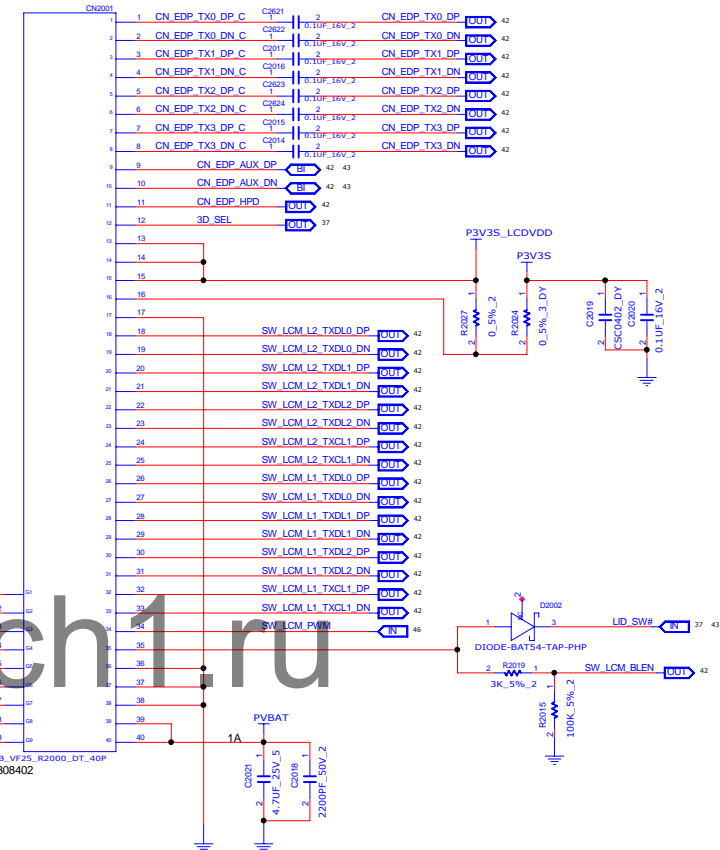
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DGPU_SELECT
L : Port1 is selected (IGPU HDMI)
H : Port2 is selected (DGPU HDMI)

INVENTEC

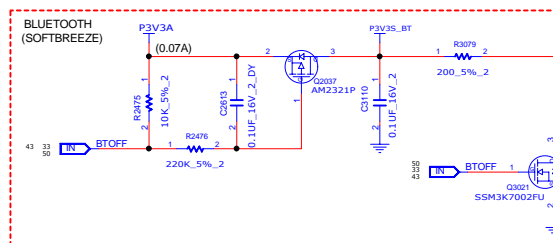
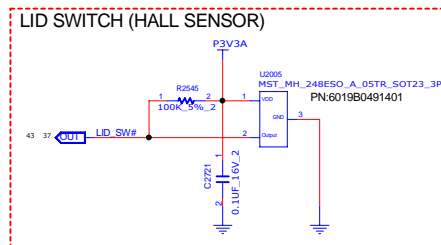
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SHEET	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



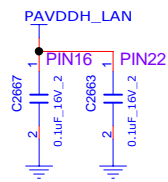
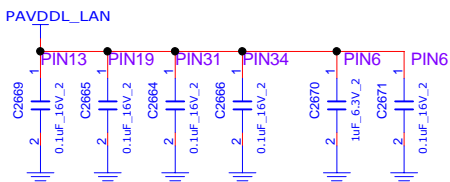
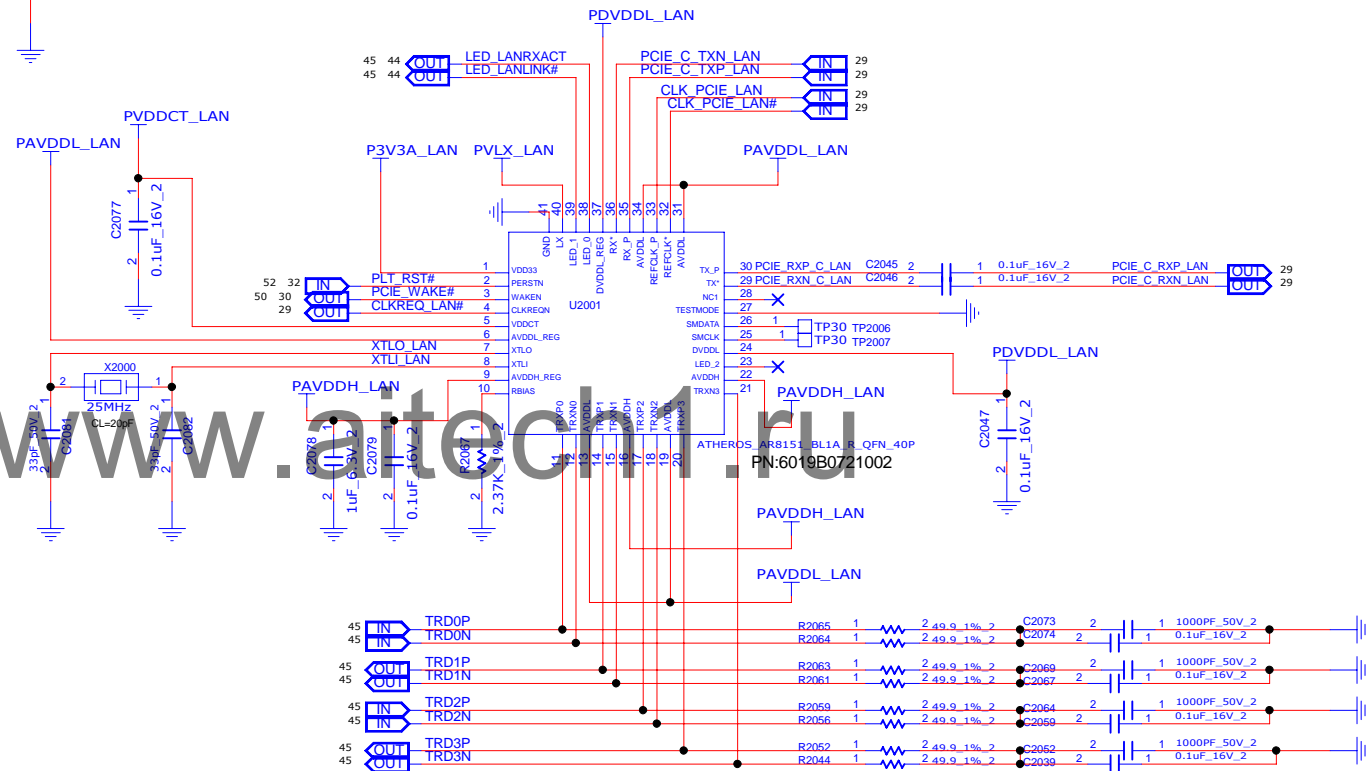
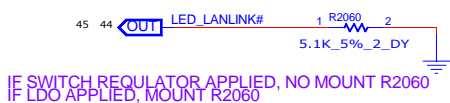
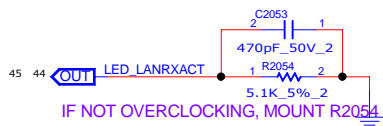
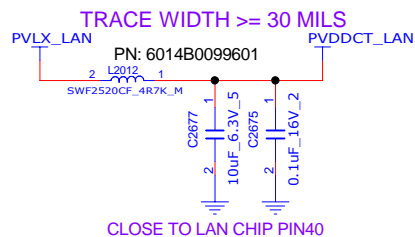
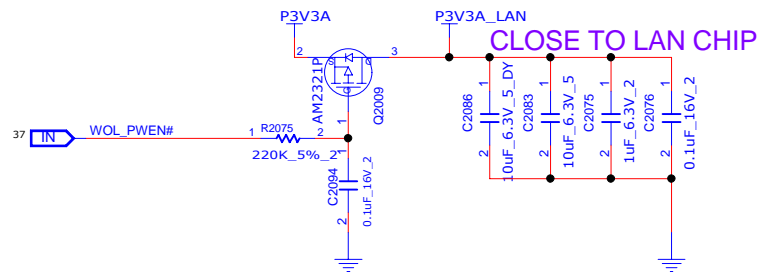
Co-Layout : DAT, CLK & AUX

The diagram illustrates the Co-Layout for DAT, CLK & AUX. It shows the connection of the LCDVDD pin (P3V3S_LCDVDD) to the AUX DP and DN pins (CN EDP AUX DP and CN EDP AUX DN) through resistors R2025, R2026, and R2027. It also shows the connection of the P3V3S pin to the DAT and CLK pins (SW LCM DAT and SW LCM CLK) through resistors R2013, R2014, and R2015. A capacitor C2003 (10PF_50V_2) is connected to the P3V3S pin. The diagram is labeled with component values and pin numbers.

	eDP	LVDS
MOUNT Ra, Rb		MOUNT Rc, Rd
		OPEN Ra, Rb



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE C	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV X01
SHEET 43 of 61			



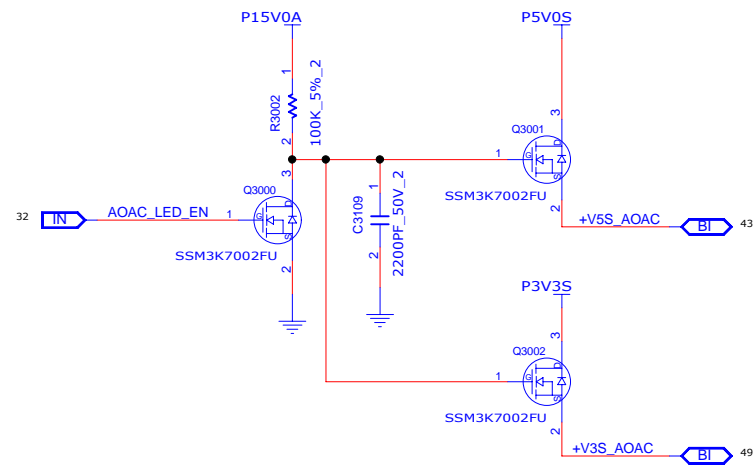
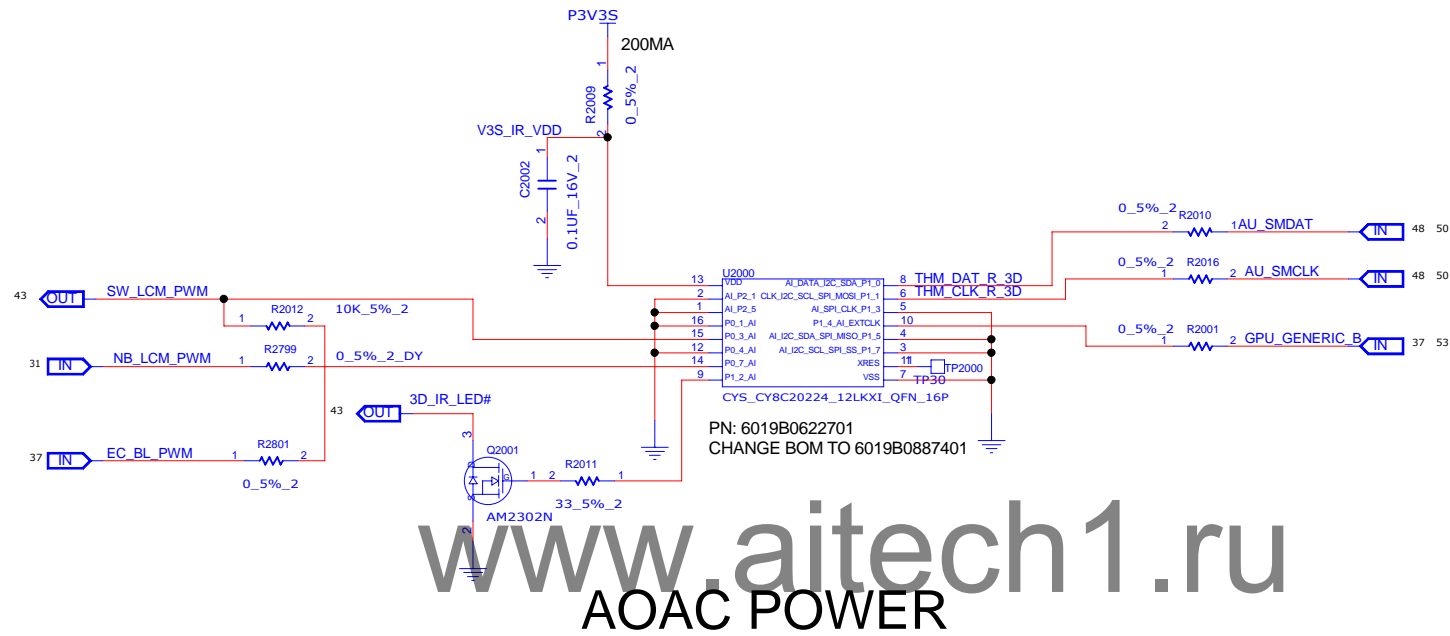
INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
LAN CHIP			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

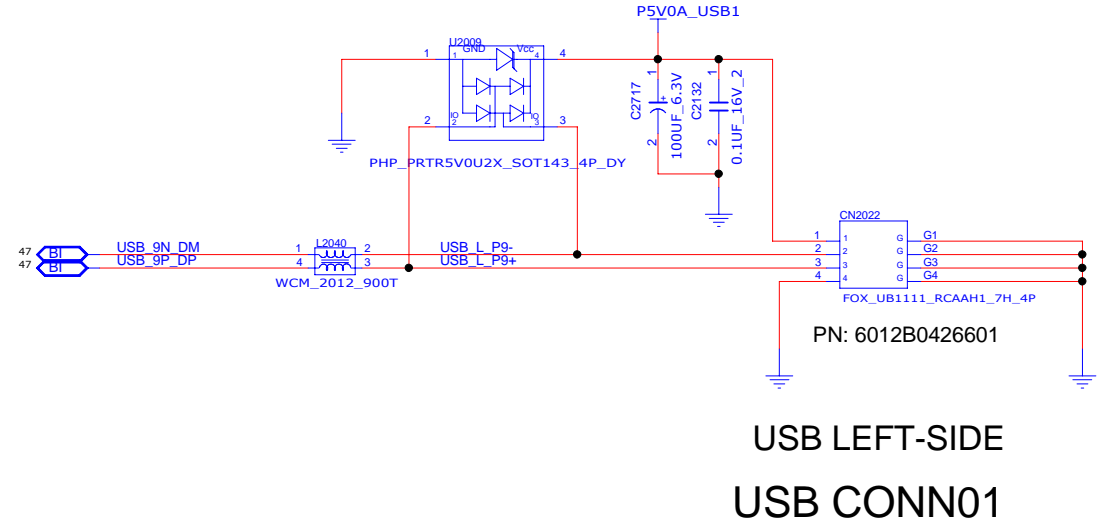
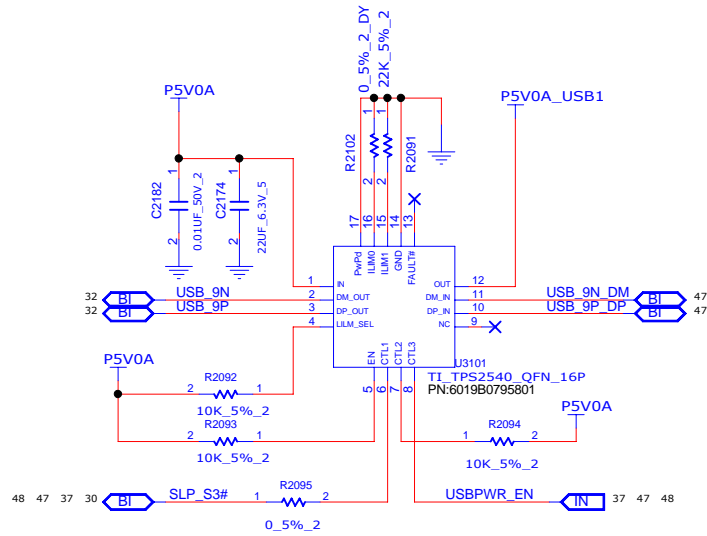
SHEET 44 of 61

3D PWM CONTROL



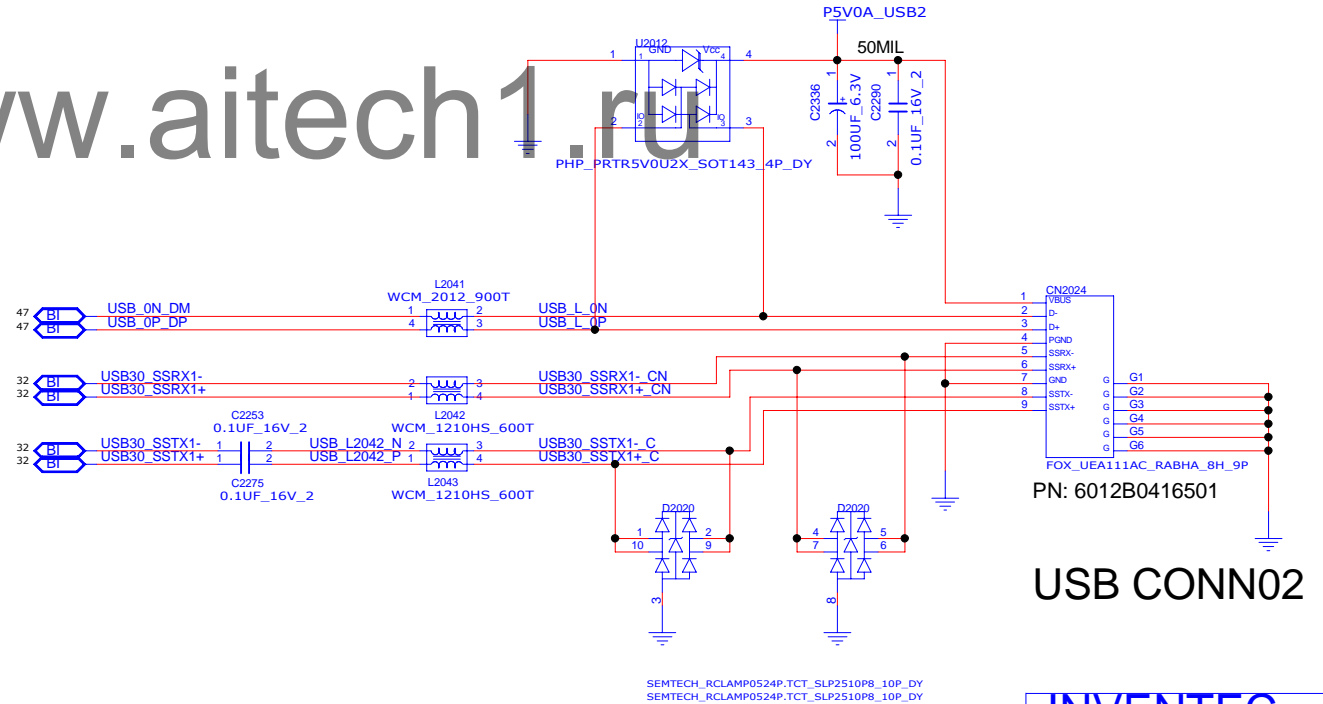
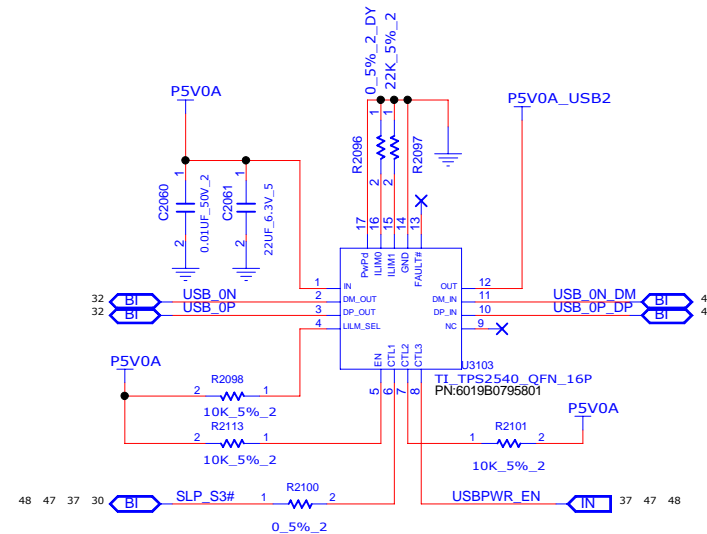
INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 46 of 61			

USB 2.0 / 3.0 CN



USB LEFT-SIDE
USB CONN01

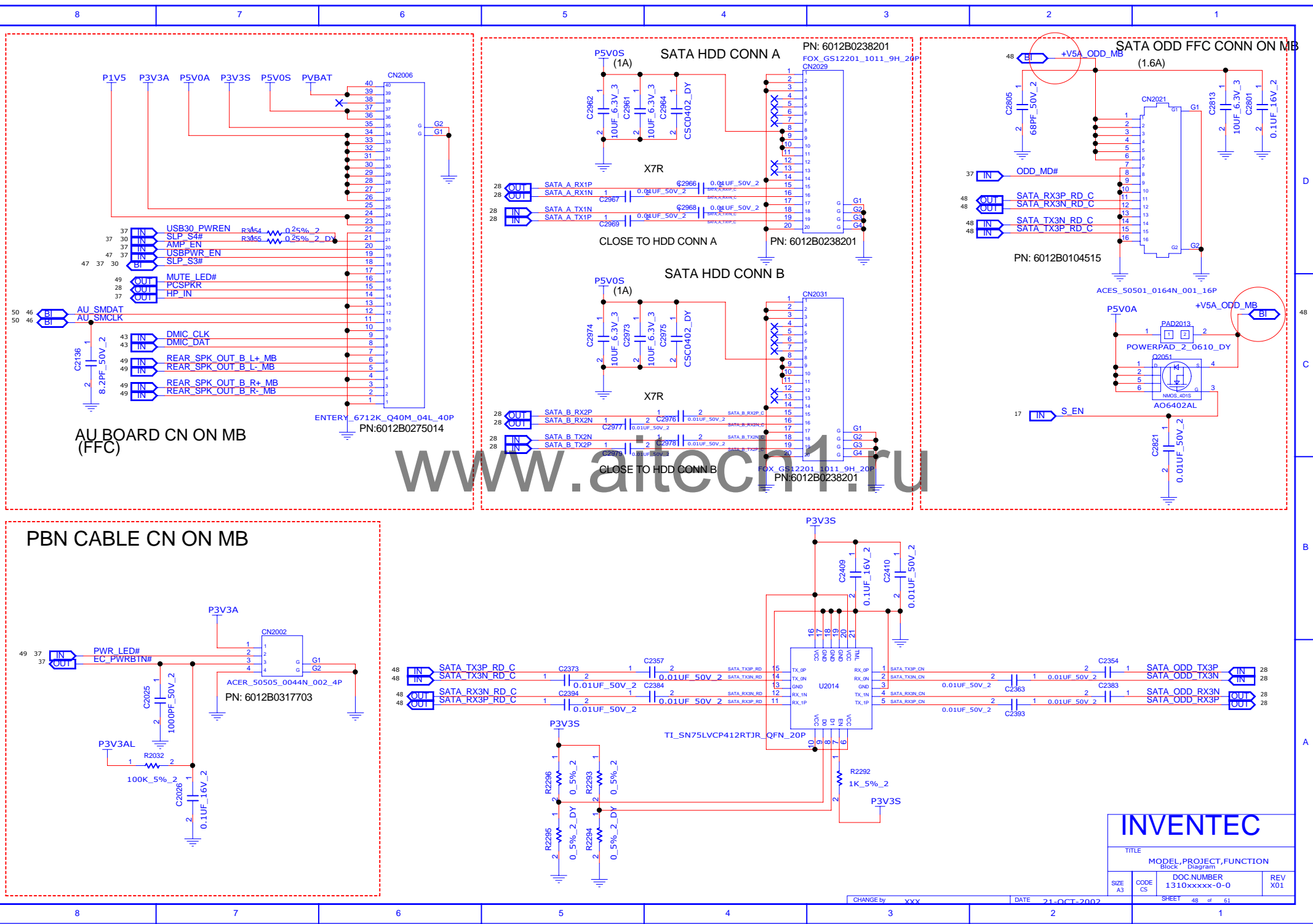
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USB CONN02

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 47 of 61			

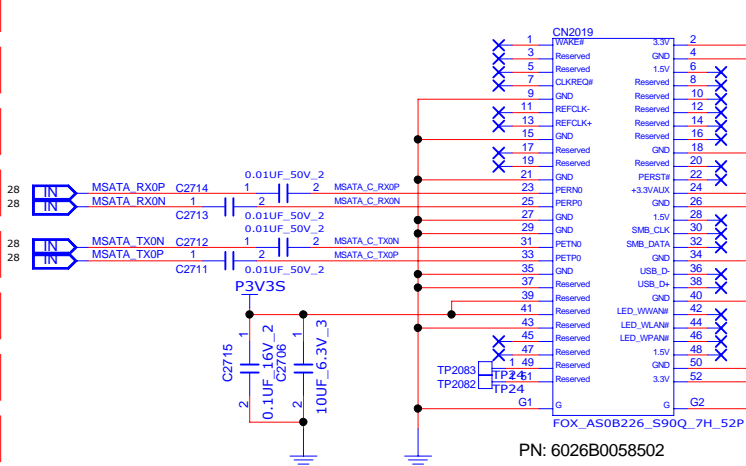
CHANGE by XXX DATE 21-OCT-2002



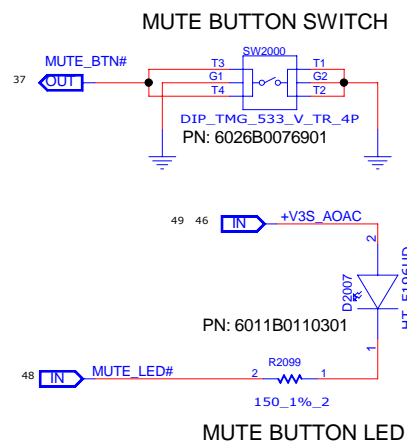
www.aitech1.ru

INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxxx-0-0	X01	
SHEET 48 of 61				

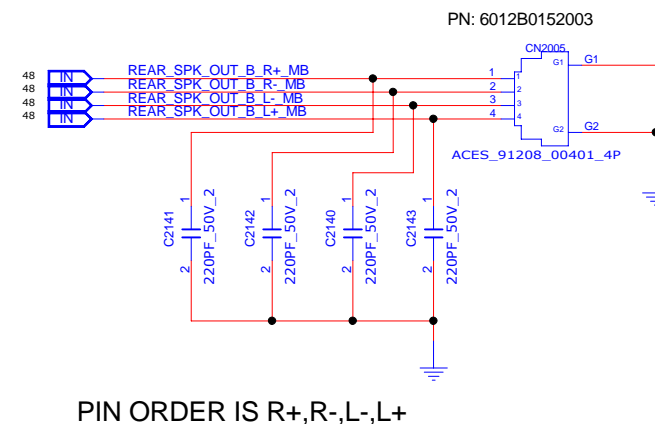
M-SATA BOARD CN ON MB



MUTE BOARD CN ON MB

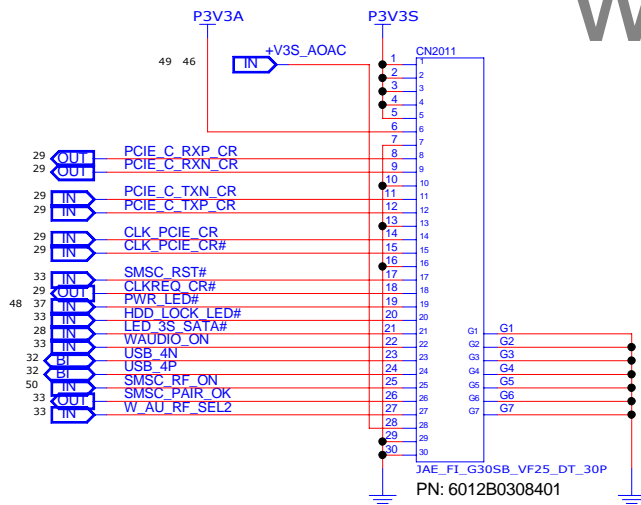


REAR SPEAKER CNTR ON MB



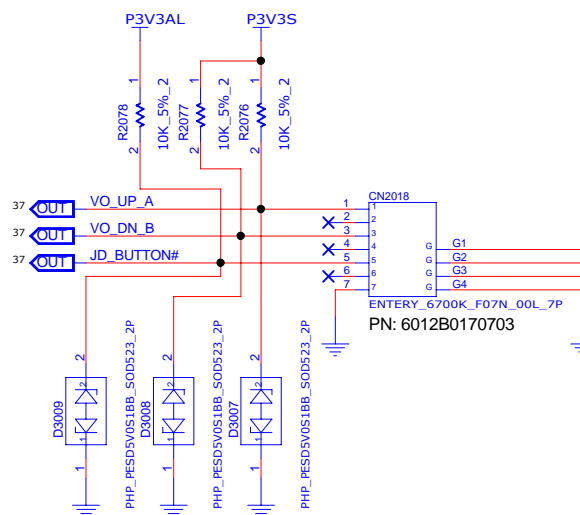
WIRELESS AUDIO COMBO

CARD READER CNTR



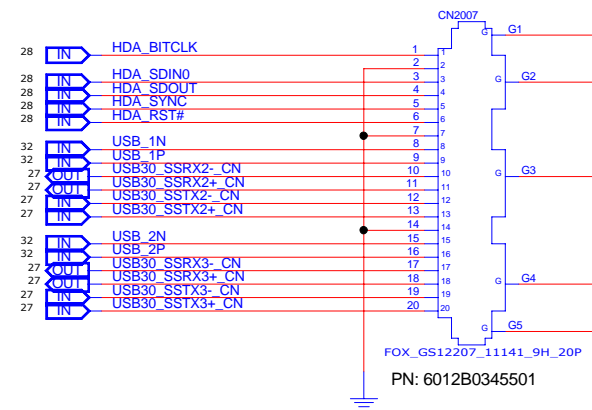
JOG DIAL CN ON MB

JOG DIAL CNTR



AU-USB30 CN ON MB

(WIRE OR COXIAL)



WIRELESS AUDIO COMBO

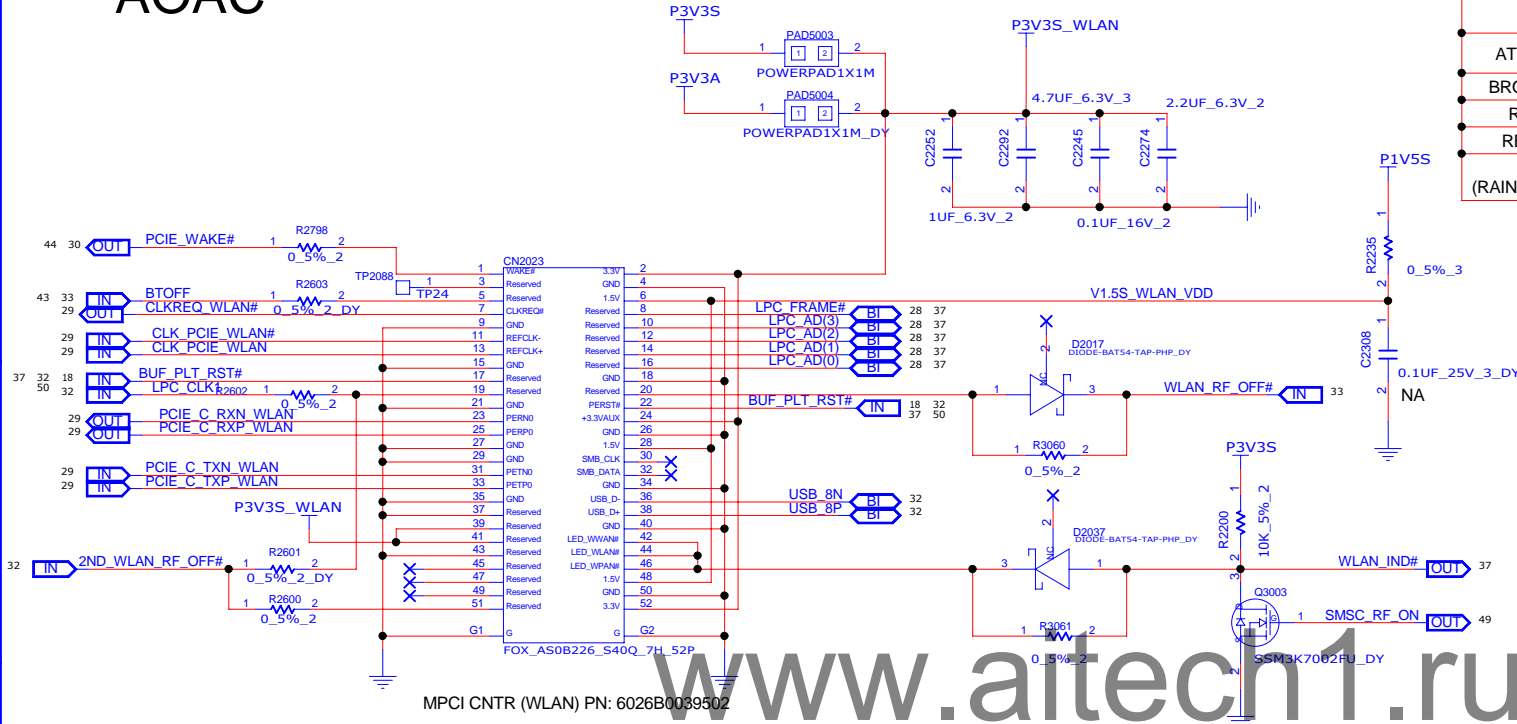
INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

AOAC

WLAN CONN (MINICARD)

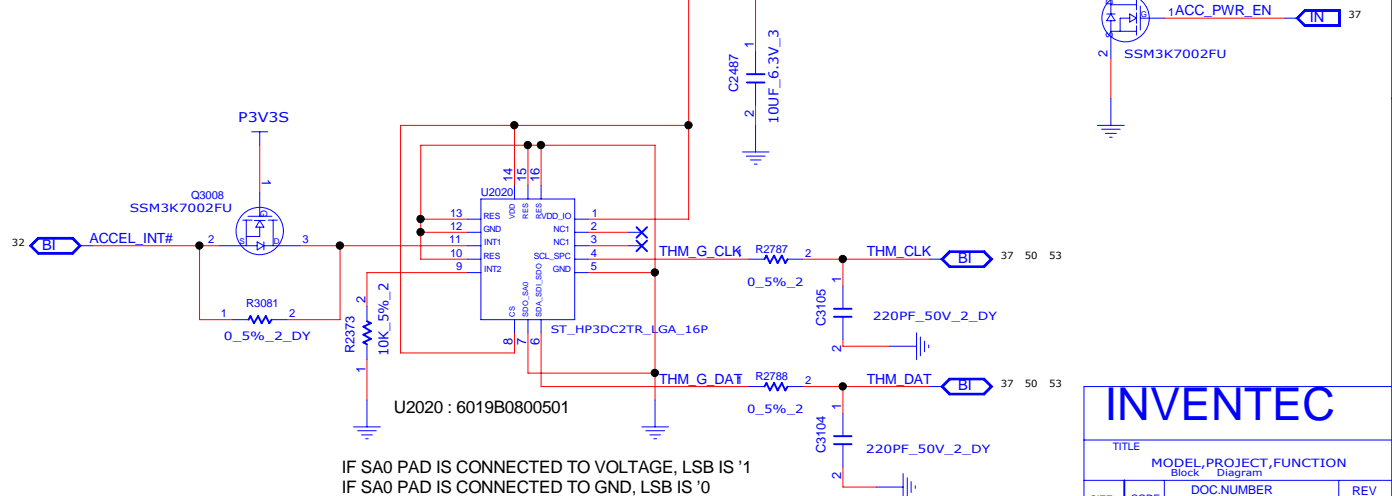
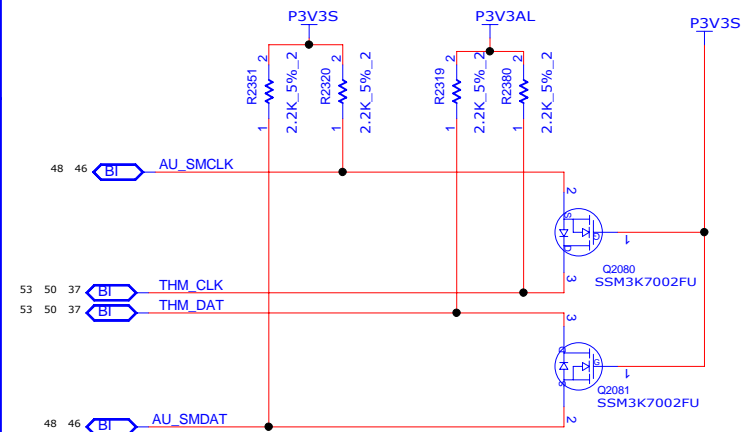
	PIN 5	PIN 19	PIN 51
ATHEROS			V (RESERVED)
BROADCOM	V (RESERVED)		
RALINK	V (RESERVED)		
REALTEK	V (RESERVED)		
INTEL (RAINBOW PEAK)		V (ES2 SAMPLE / WW27)	V (QS SAMPLE/WW42)



MPCI CNTR (WLAN) PN: 6026B0039502

AUB_SMBUS

HARDDRIVE PROTECTION



IF SA0 PAD IS CONNECTED TO VOLTAGE, LSB IS '1'
IF SA0 PAD IS CONNECTED TO GND, LSB IS '0'
SMBUS ADDRESS IS 001100XB

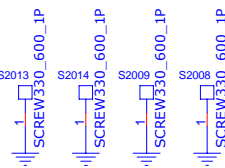
INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		50 of	61

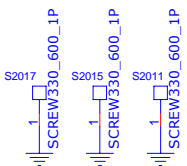
CHANGE by XXX DATE 21-OCT-2002

EMI CAP.

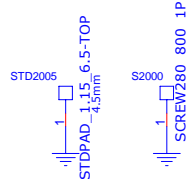
CPU screws



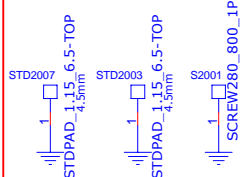
VGA screws



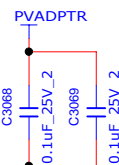
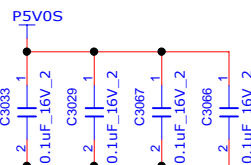
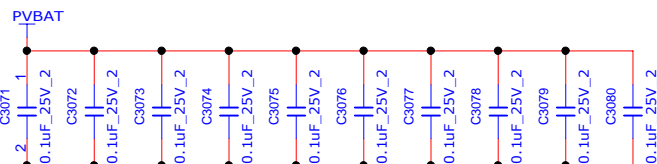
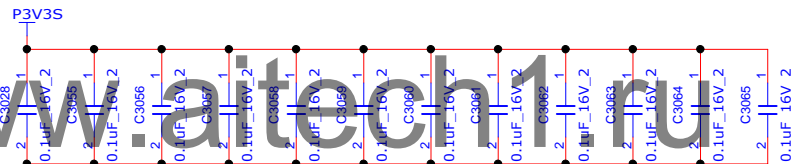
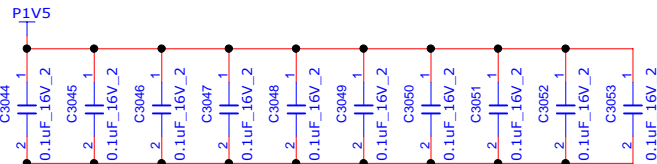
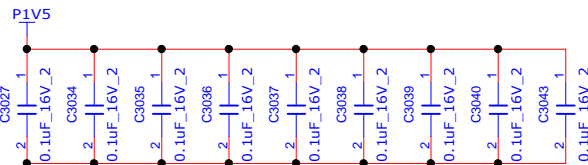
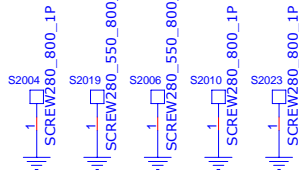
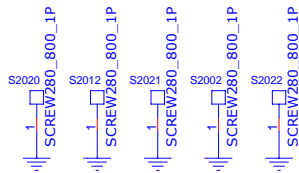
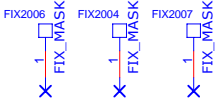
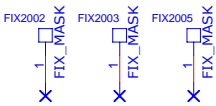
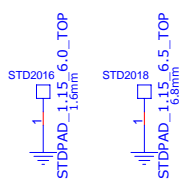
DGPU FAN screws



IGPU FAN screws



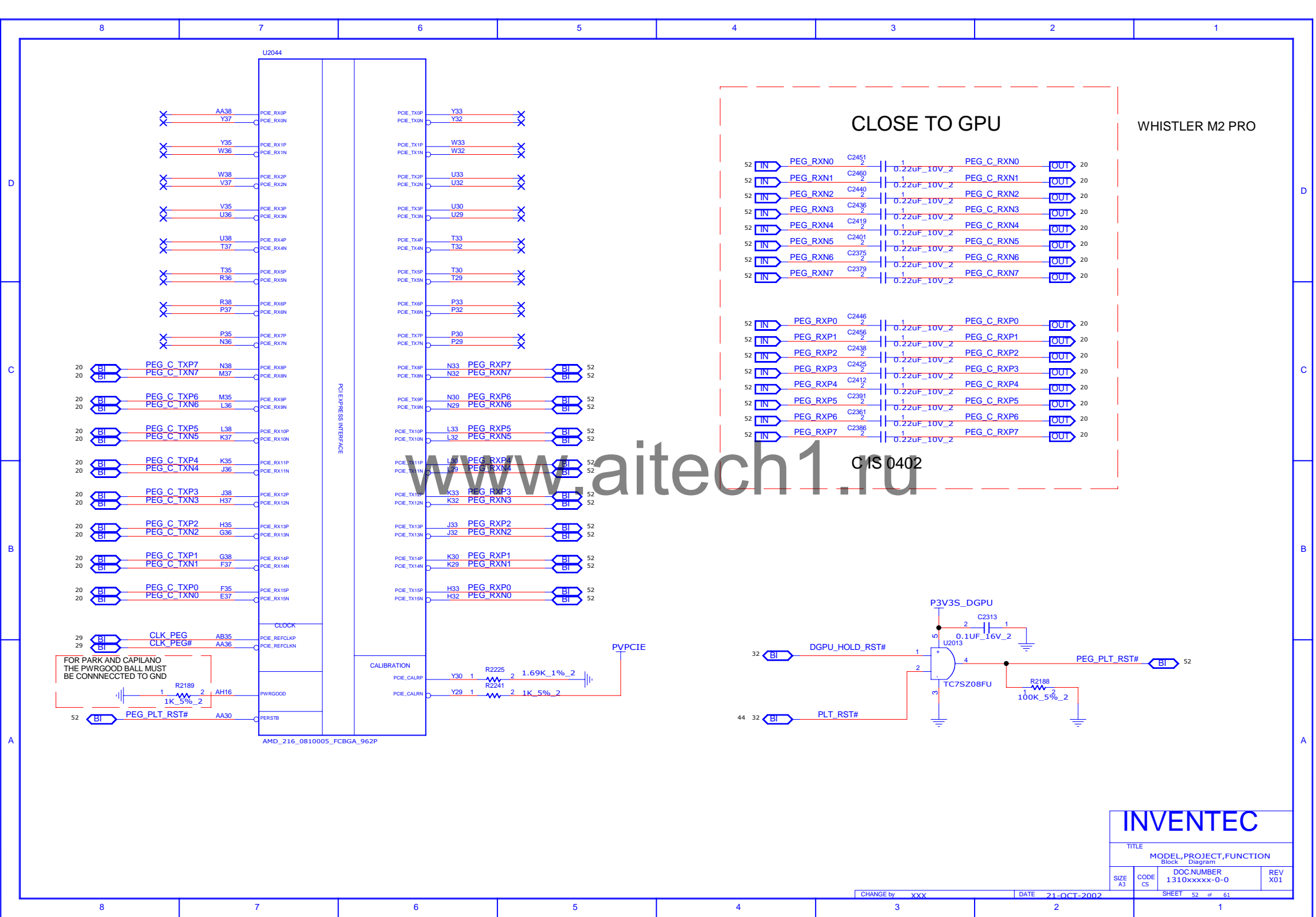
WLAN screws

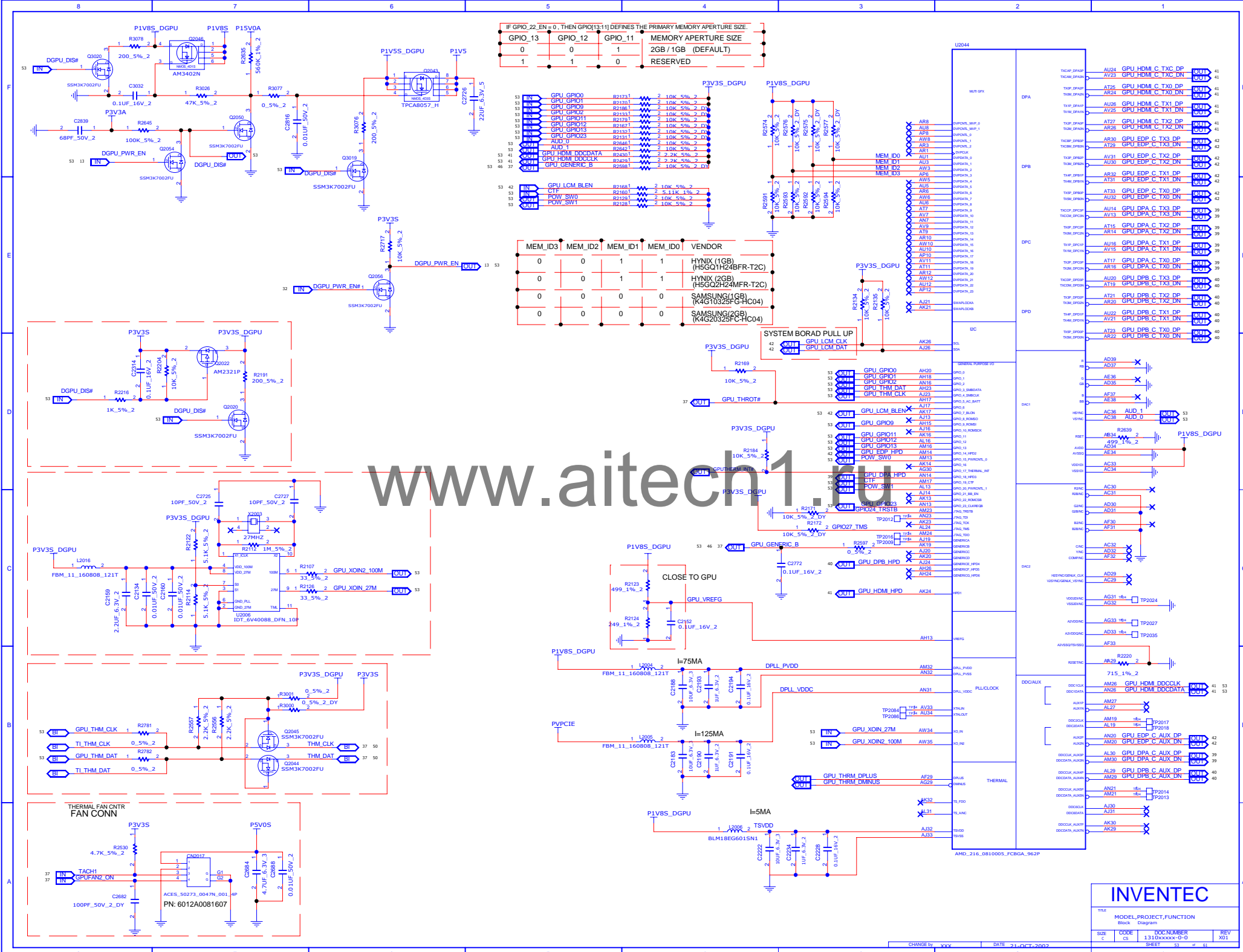


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INVENTEC

TITLE			
Block Diagram			
MODEL,PROJECT,FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

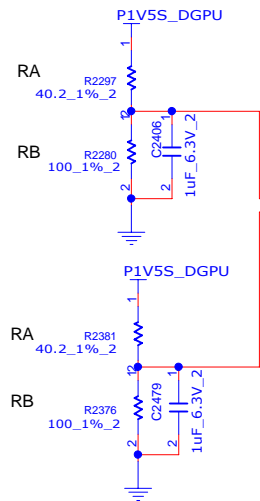




CHANNEL A

DDR3/GDDR3MEMORY STUFF OPTION

	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
RA	40.2R	40.2R	40.2R
RB	100R	100R	100R



MVREFDA GPU
MVREFSA GPU

FOR M97,BROADWAY,MADISO AND PARK ONLY



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

Block Diagram

DOC NUMBER
1310xxxxx-0-0

REV
X01

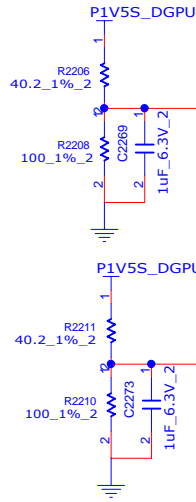
CHANGE by XXX DATE 21-OCT-2002

SHEET 54 of 61

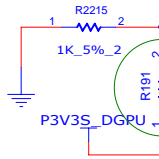
CHANNEL B

DDR3/GDDR3MEMORY STUFF OPTION

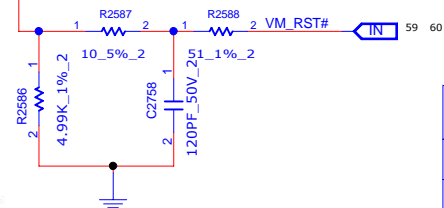
	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
RA	40.2R	40.2R	40.2R
RB	100R	100R	100R



MVREFDB_GPU
MVREFSB_GPU



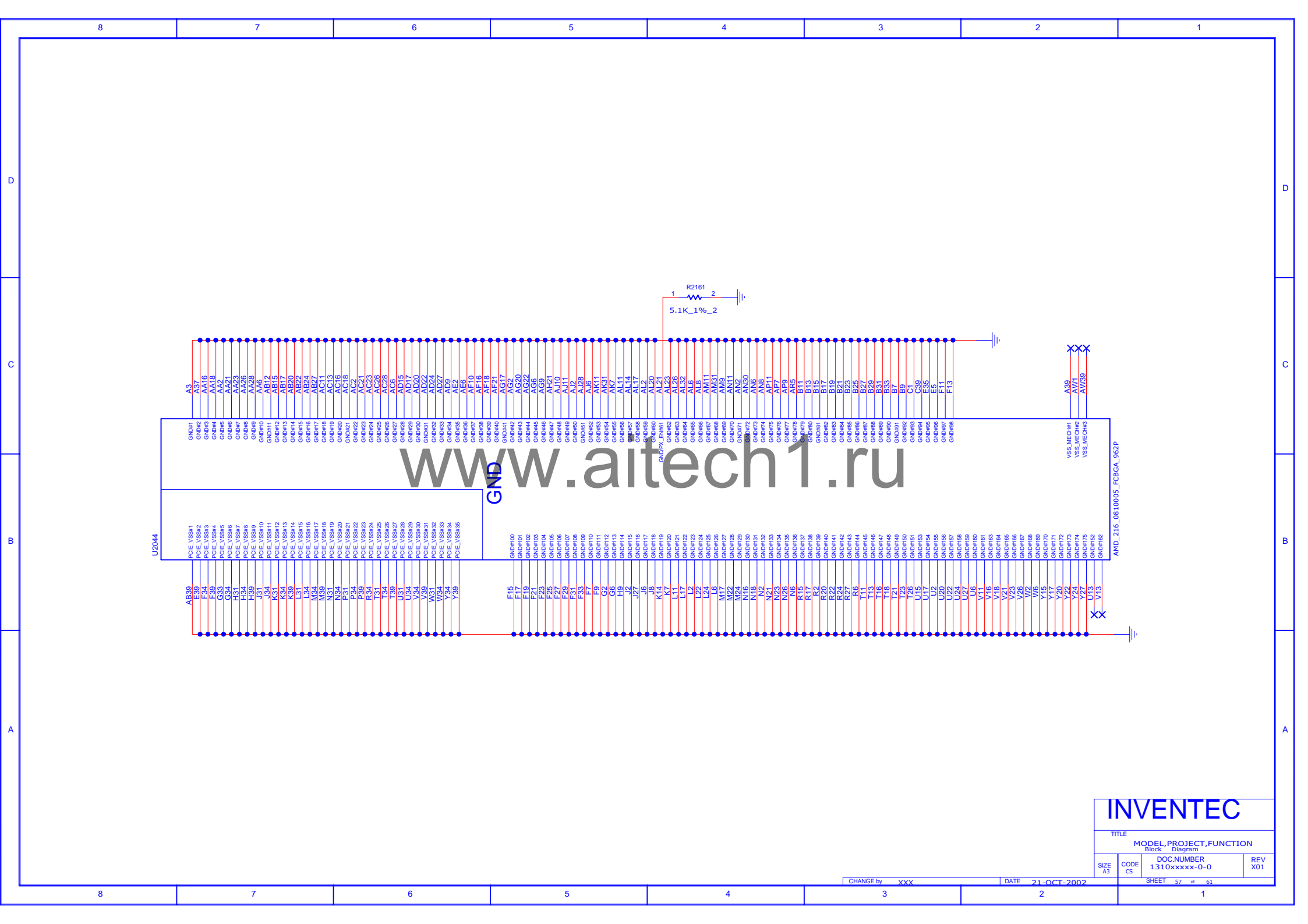
Place all these components vry close to GPU (Within 25mm)
and keep all component close to each other (within 5mm) except Rser2
* This basic topology should be used for DRAM_RST for DDR3/GDDR3/GDDR5
These Capacitors and Resistor values are an example only.
The Series R and || Cap values will depend on the DRAM load and will have to be
calculated for different Memory , DRAM Load and board to pass Reset Signal Spec.



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
DOC NUMBER			
1310xxxxx-0-0			
SIZE	CODE	REV	
A3	CS	X01	
SHEET 55 of 61			

CHANGE by XXX DATE 21-OCT-2002



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INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

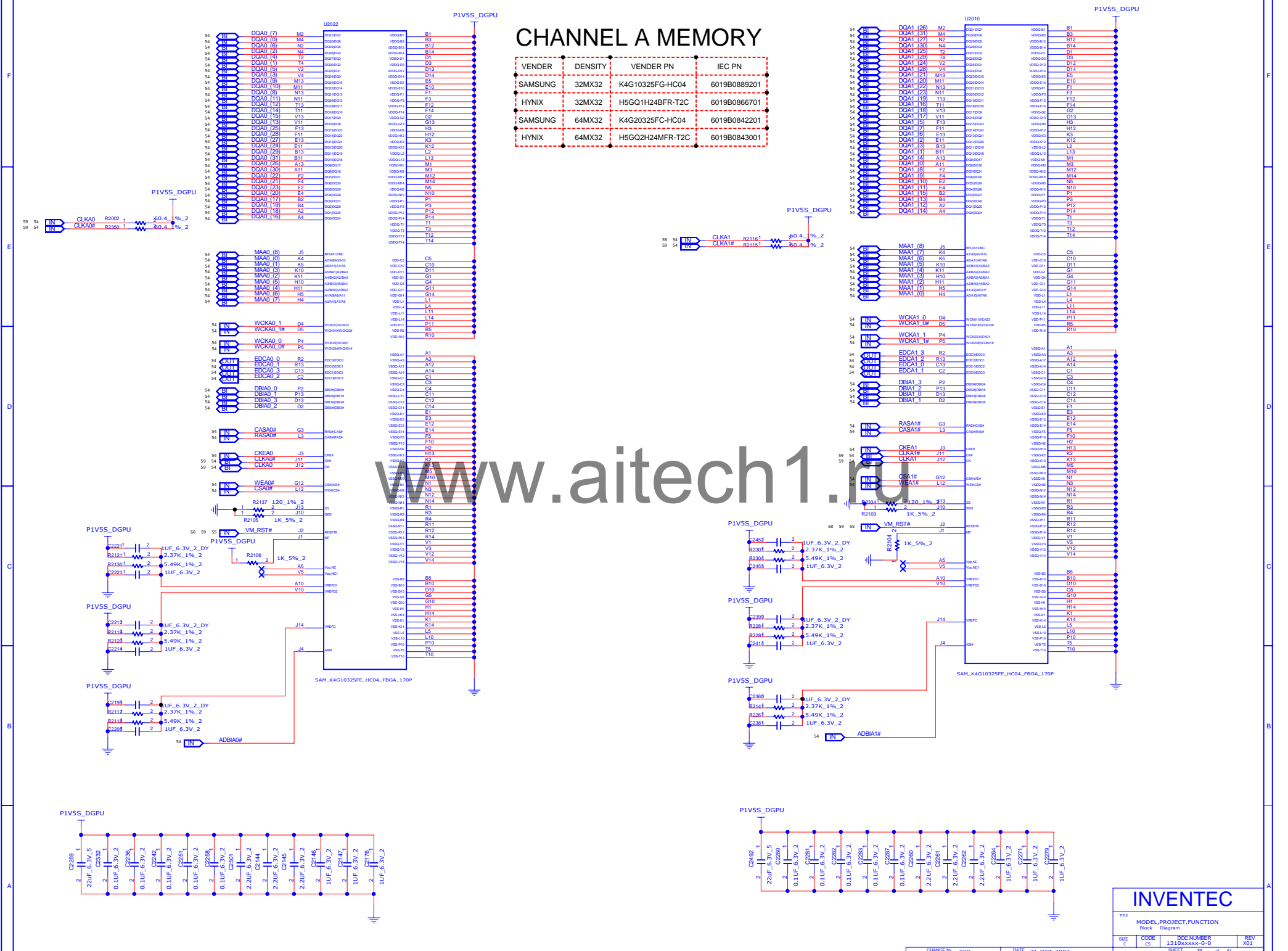
CHANGE by XXX DATE 21-OCT-2002

SHEET 57 of 61

CHANNEL A MEMORY

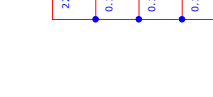
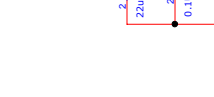
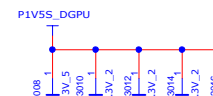
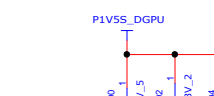
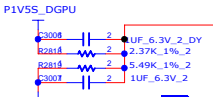
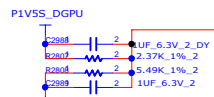
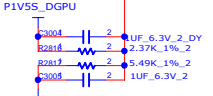
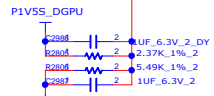
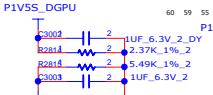
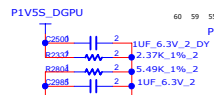
VENDER	DENSITY	VENDER PN	IEC PN
SAMSUNG	32MX32	K4G10325F0-HC04	6019B0889201
HYNIX	32MX32	H5GQ1H24BFR-T2C	6019B0866701
SAMSUNG	64MX32	K4G20325FC-HC04	6019B0842201
HYNIX	64MX32	H5GQ2H24MFR-T2C	6019B0843001

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SAMSUNG P/N = 6019B069401
HYNIX P/N = 6019B
CIMONDA P/N =

CHANNEL B MEMORY

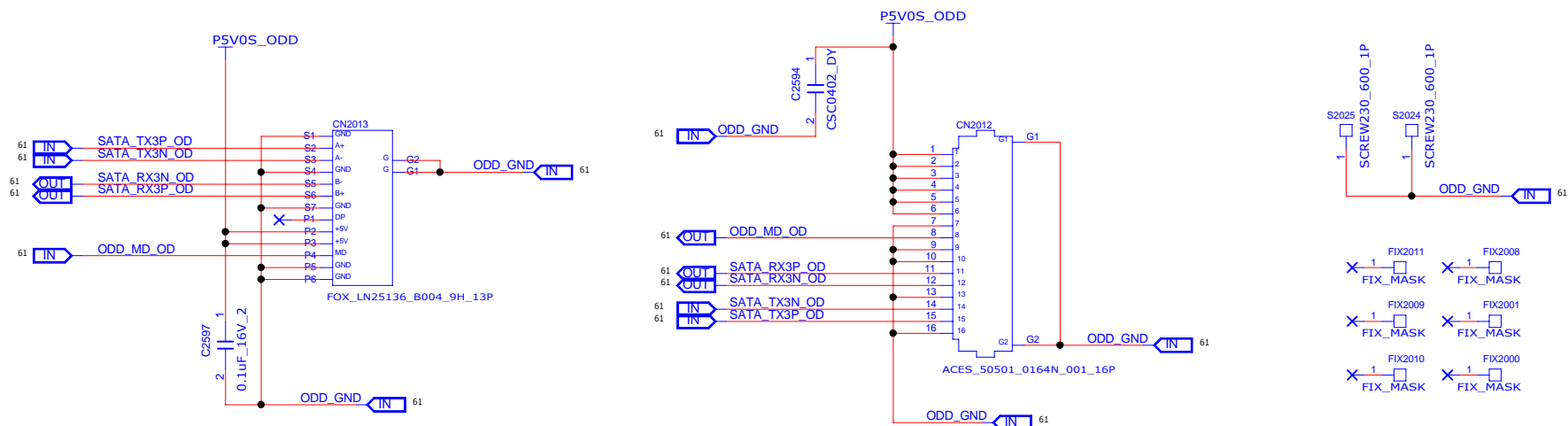


INVENTEC

MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310XAXX-0-0	X01
SHEET 60 OF 61			

CHANGE BY: YXX DATE: 21-OCT-2002

ODD Board: 6050AXXXXXXX



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INVENTEC

TITLE
MODEL, PROJECT, FUNCTION
Block Diagram
DOC NUMBER
1310xxxxx-0-0
REV
X01

CHANGE by XXX DATE 21-OCT-2002

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